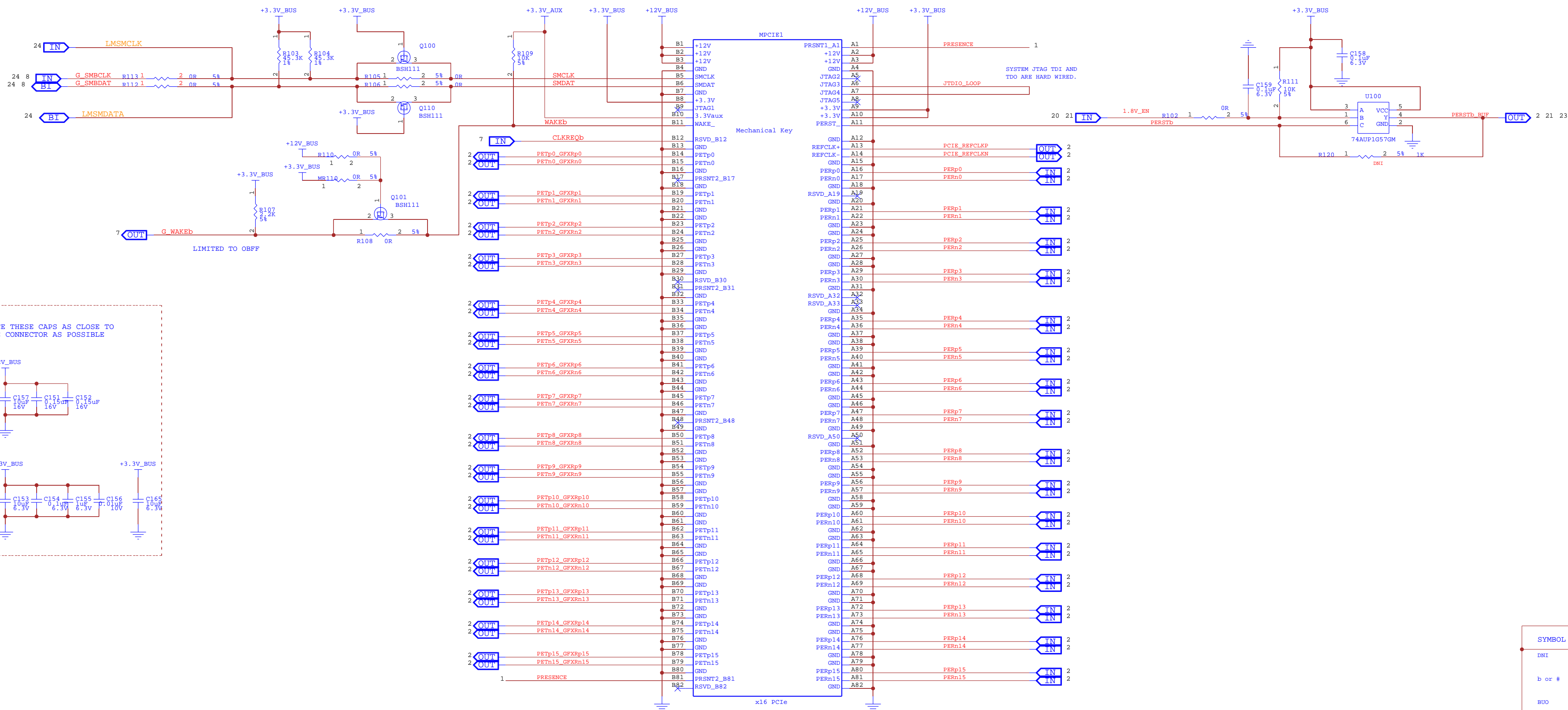


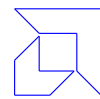
(1) PCI-EXPRESS EDGE CONNECTOR



PLACE THESE CAPS AS CLOSE TO
PCIe CONNECTOR AS POSSIBLE

SYMBOL LEGEND	
	DO NOT INSTALL
b or #	ACTIVE LOW
BUO	BRING UP ONLY
	DIGITAL GROUND
	ANALOG GROUND

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SHEET: PCIE EDGE CONNECTOR

DATE: Wed Apr 13 17:02:06 2016

REV: 1.0

SHEET NUMBER: 1 OF 26

DOCUMENT NUMBER: 105_D009XX_00

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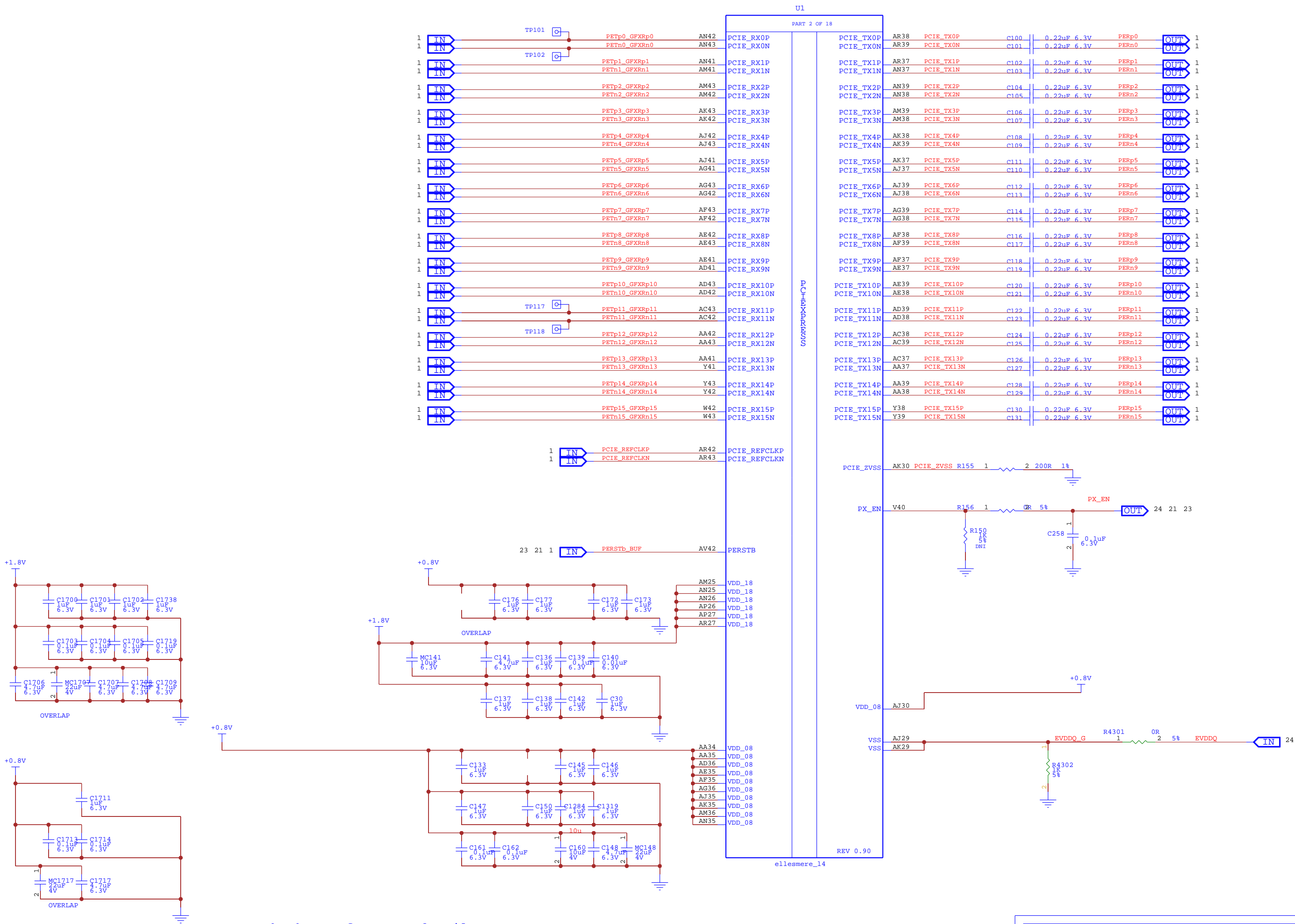
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TITLE:

Ellesmere XT/PRO GD5 8L

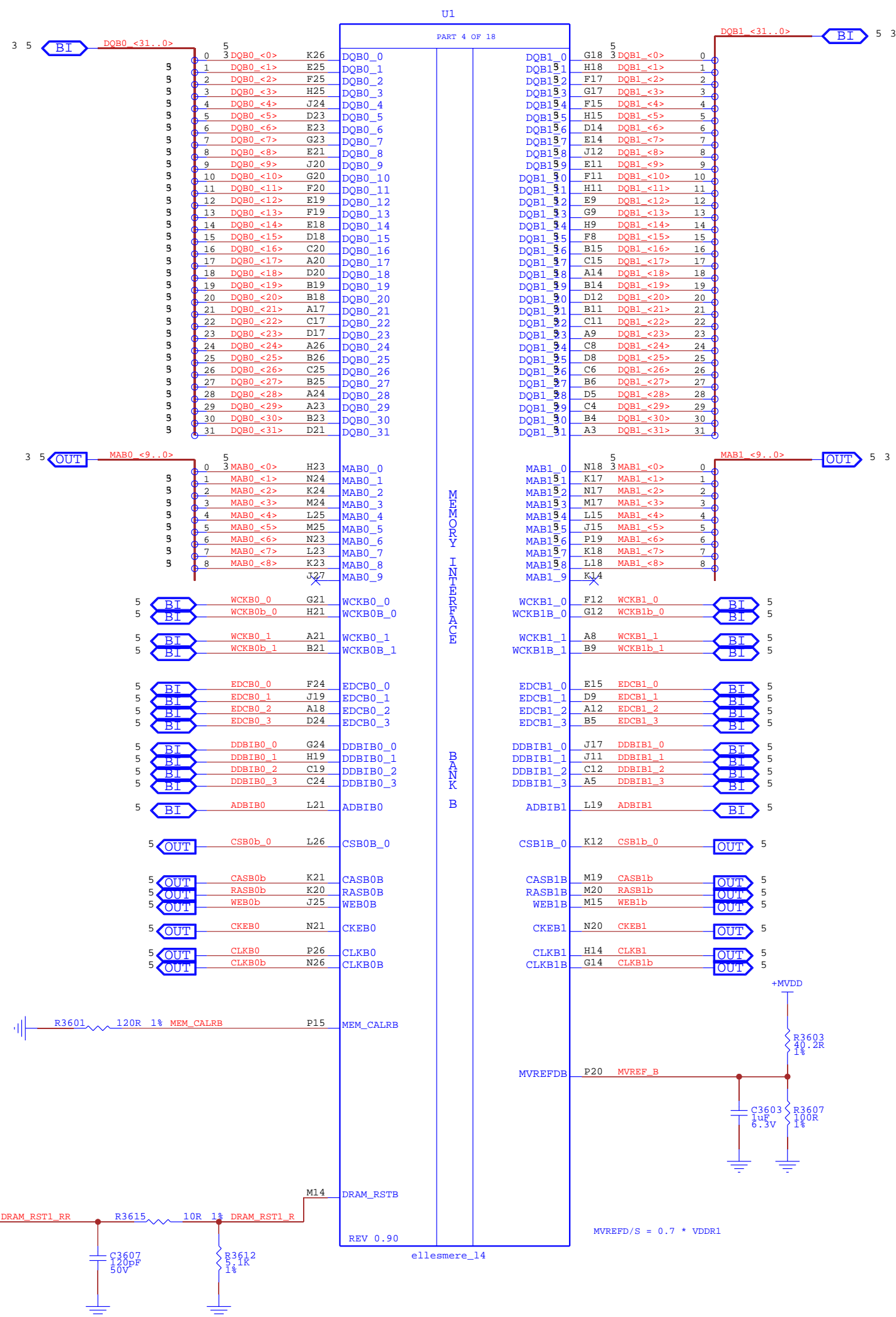
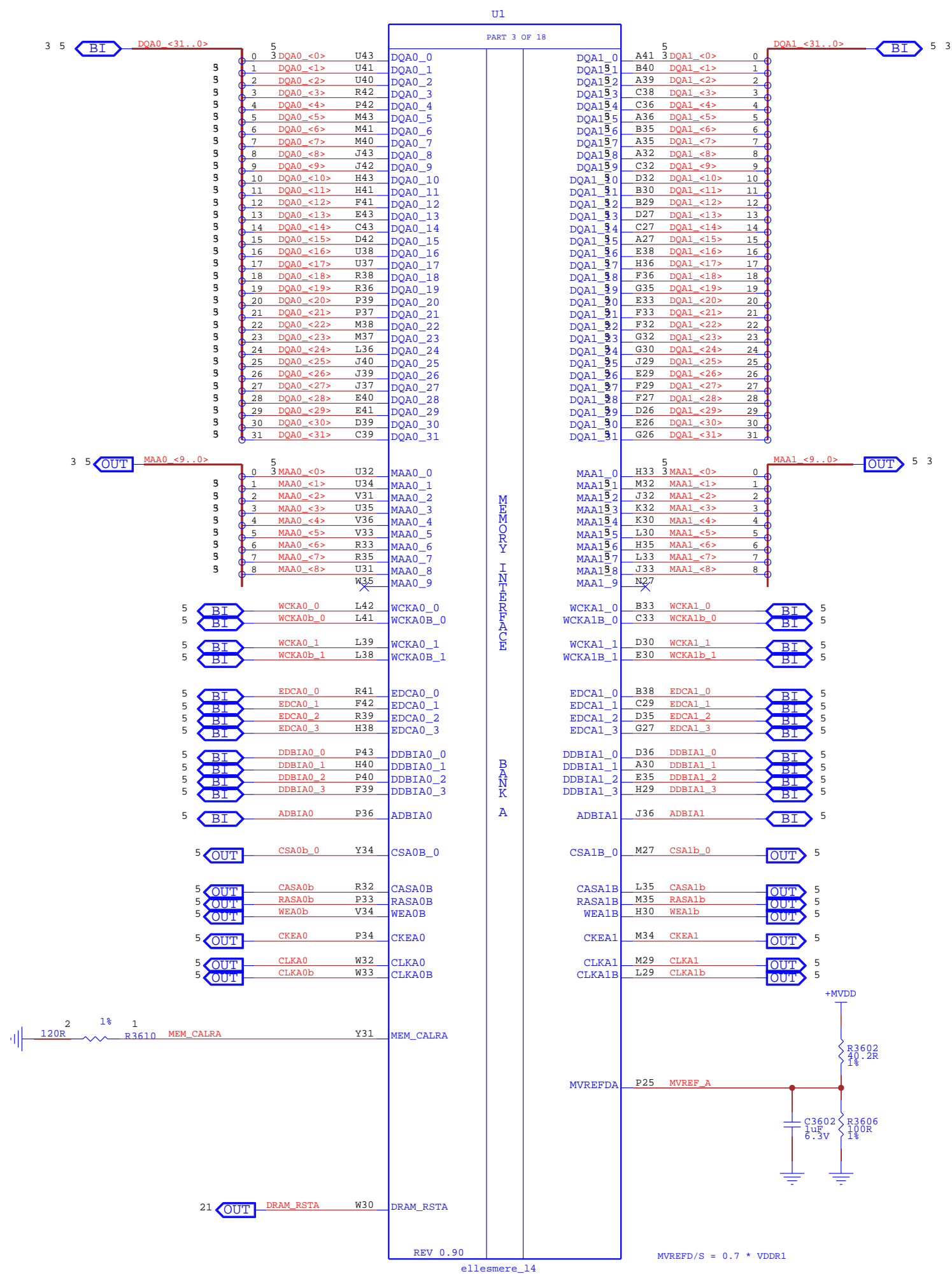
(2) ELLESMERE PCIE INTERFACE



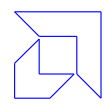
Check BOM for more detail

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SHEET: ELLESMERE PCIE INTERFACE		TITLE: Ellesmere XT/PRO GD5 8L	
DATE: Wed Apr 13 17:02:07 2016		REV: 1.0	
SHEET NUMBER: 2 OF 26			
DOCUMENT NUMBER: 105_D009XX_00			

(3) ELLESMERE MEM INTERFACE CH A/B



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SHEET: ELLESMERE MEM CH AB

DATE: Wed Apr 13 17:02:15 2016

REV: 1.0

SHEET NUMBER: 3 OF 26

TITLE:

Ellesmere XT/PRO GD5 8L

DOCUMENT NUMBER: 105_D009XX_00

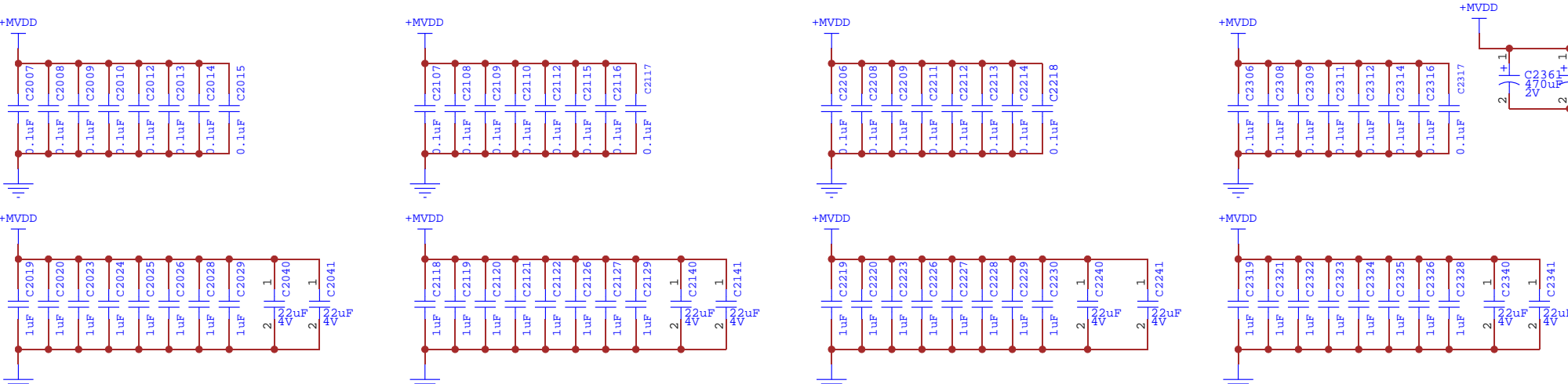
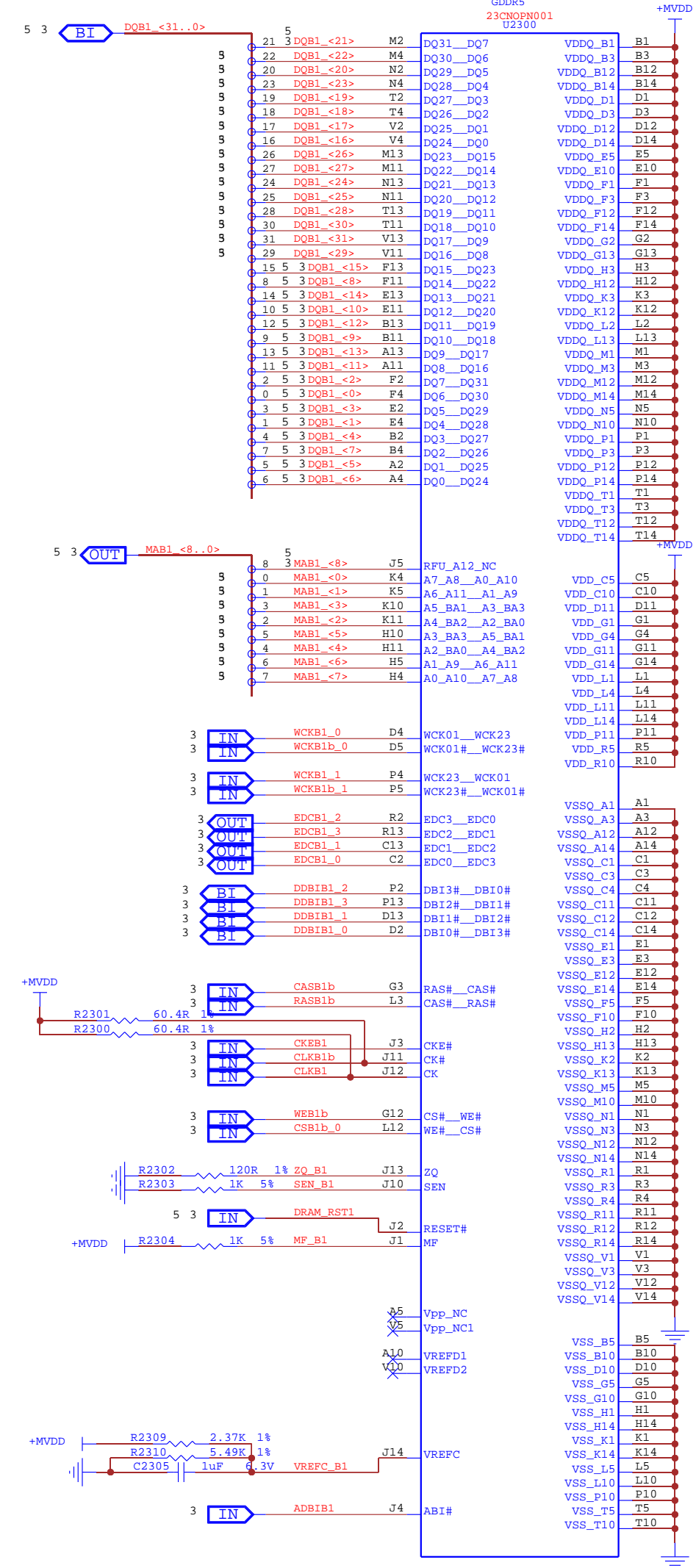
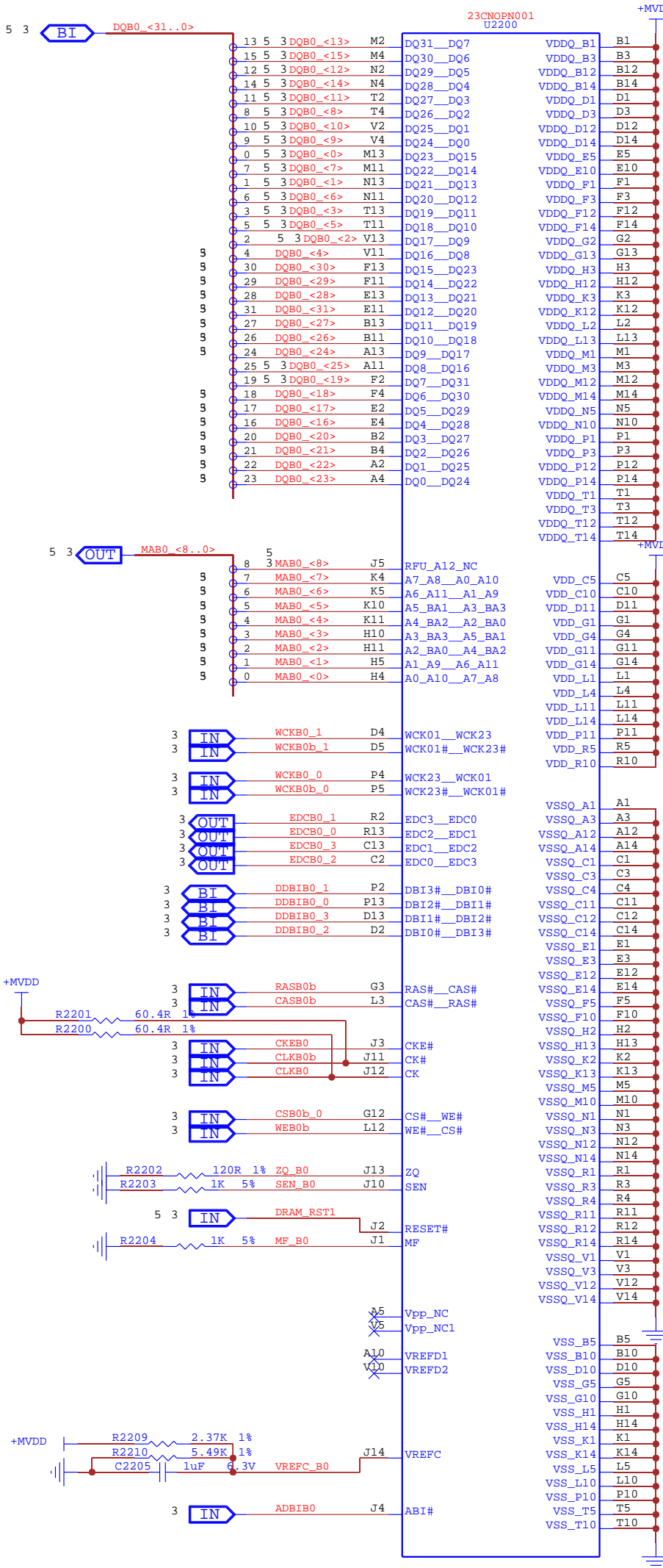
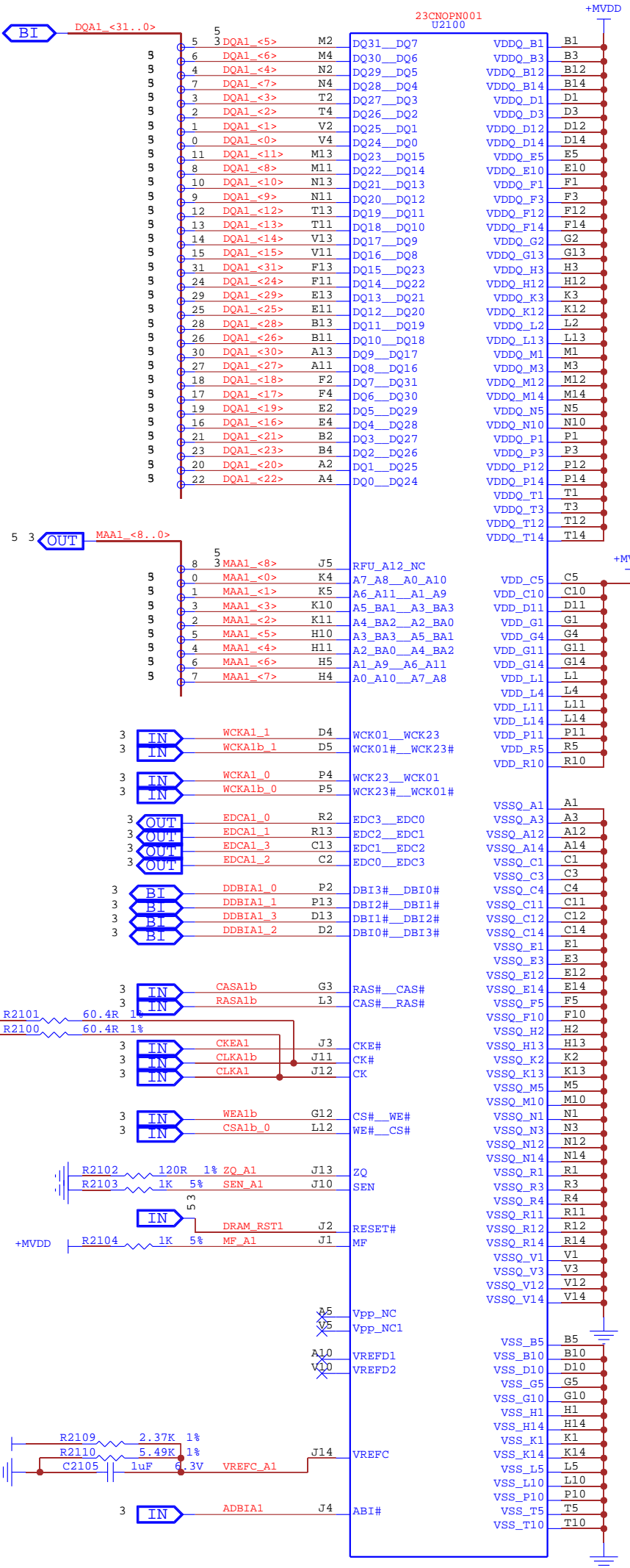
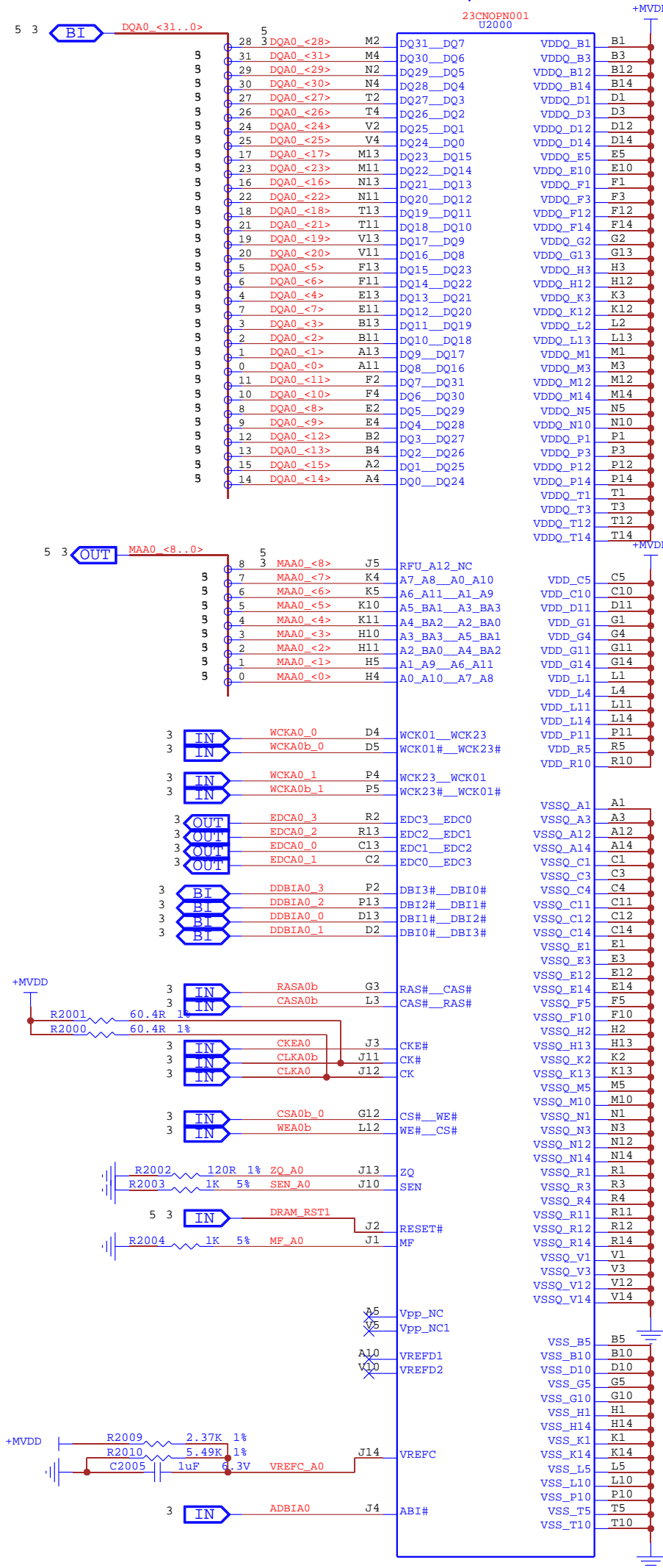
D



A

DOCUMENT NUMBER: 105_D009XX_00

(5) GDDR5 MEMORY CH A/B



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SHANGHAI, CHINA 201203

SHEET: GDDR5 MEM CH AB

DATE: Wed Apr 13 17:02:16 2016

REV: 1.0

SHEET NUMBER: 5 OF 26

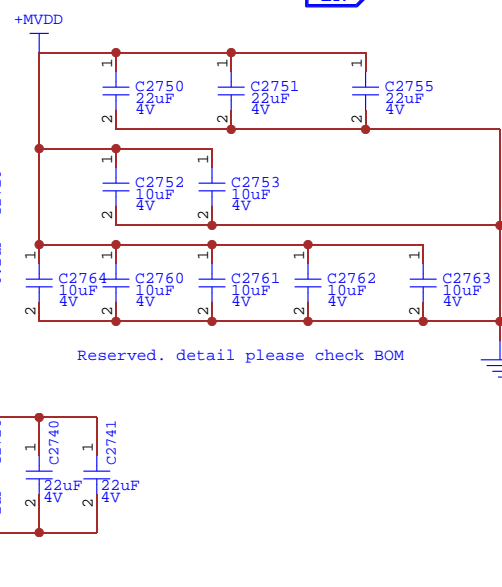
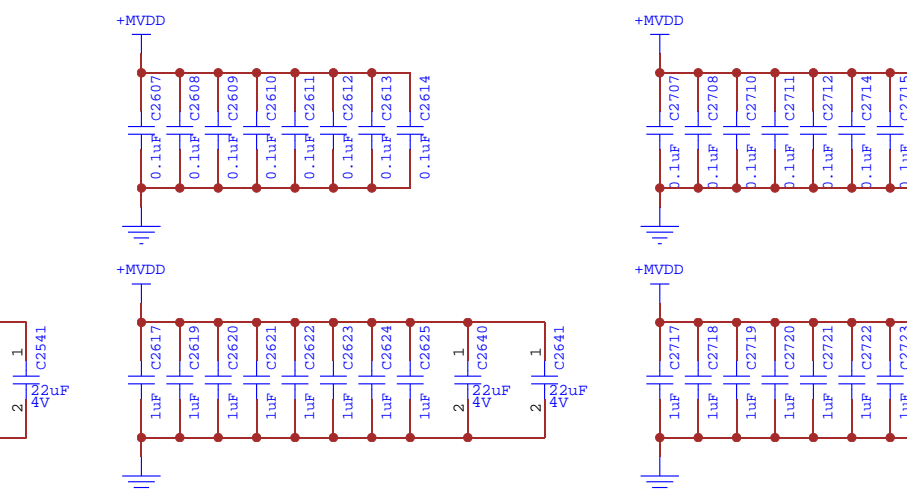
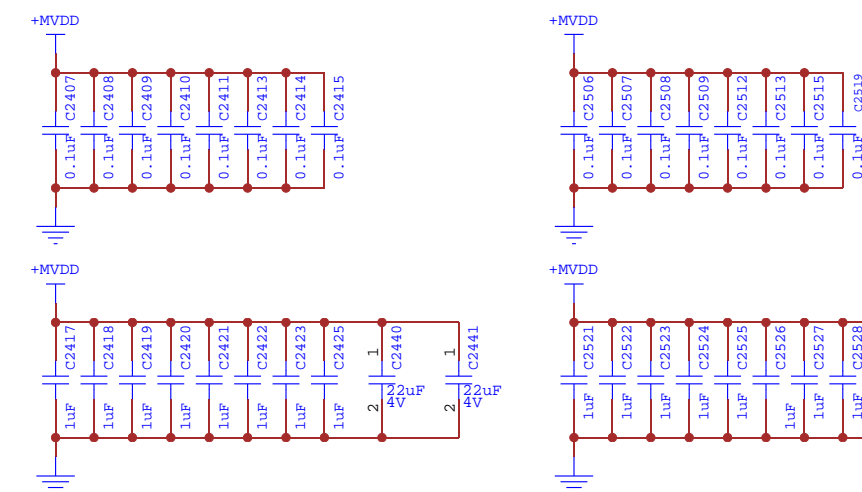
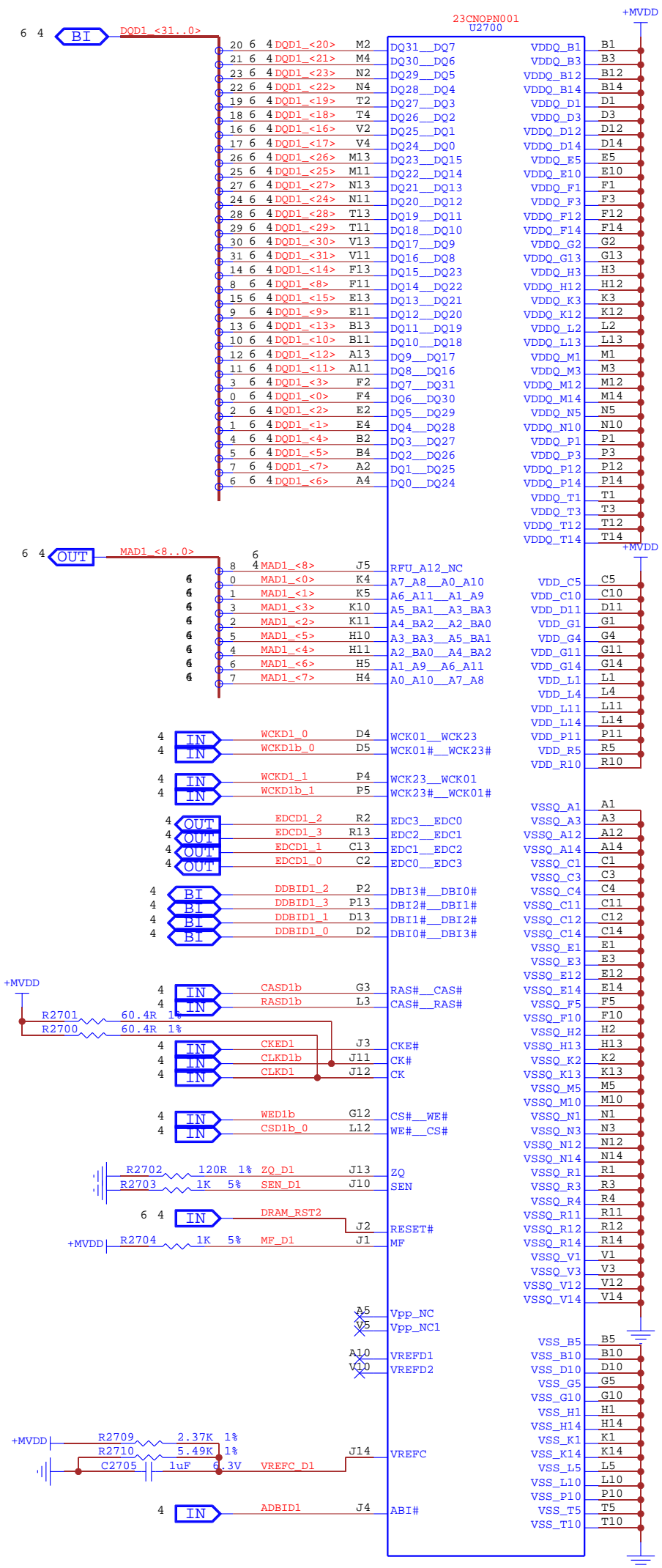
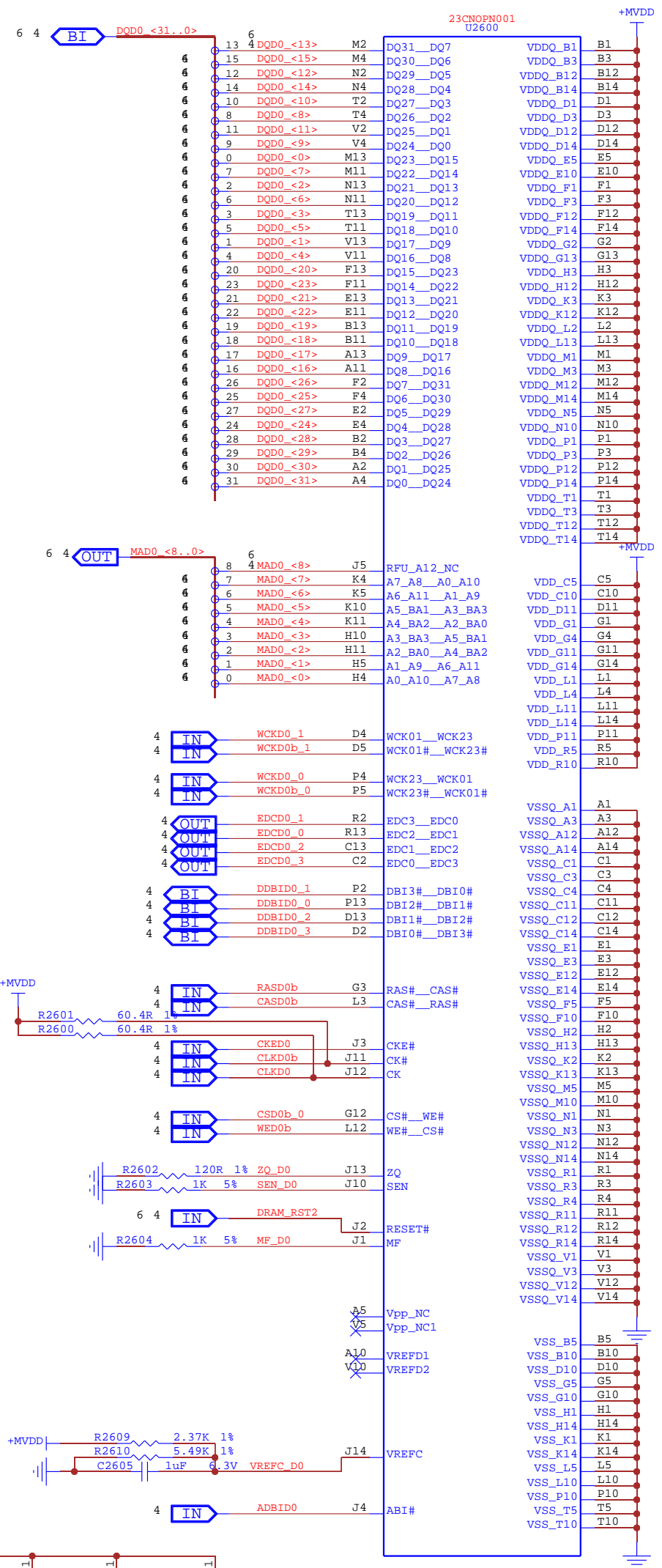
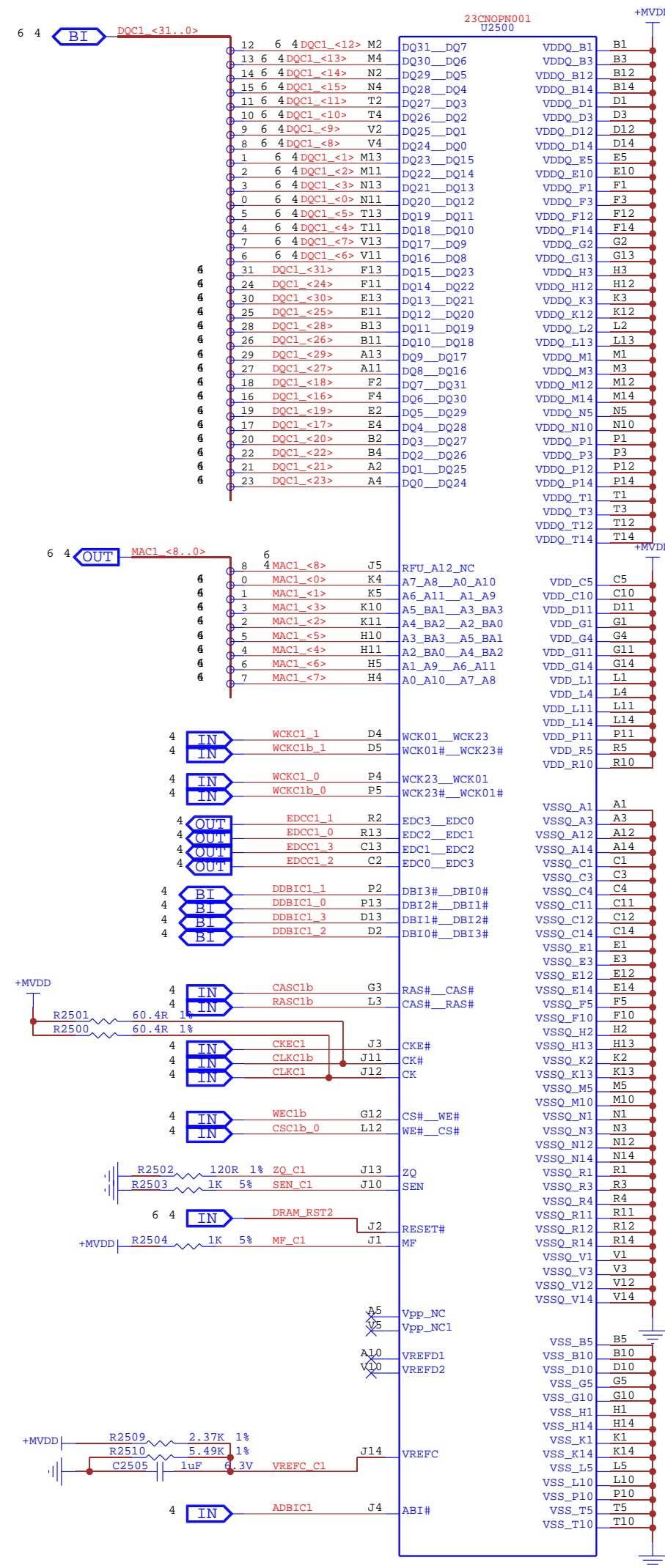
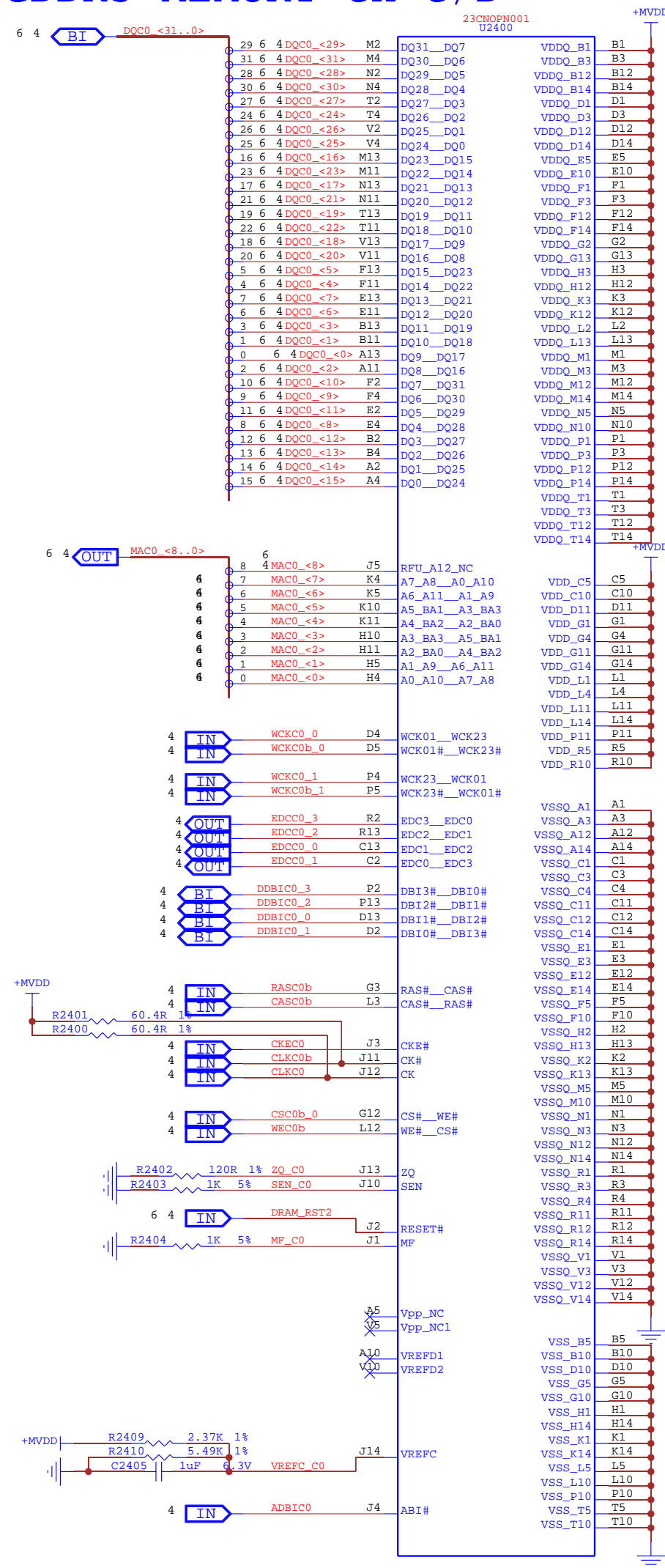
DOCUMENT NUMBER: 105_D009XX_00

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(6) GDDR5 MEMORY CH C/D



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SHEET: GDDR5 MEM CH CD

DATE: Wed Apr 13 17:02:17 2016

SHEET NUMBER: 6 OF 26

DOCUMENT NUMBER: 105_D009XX_00

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TITLE:

Ellesmere XT/PRO GD5 8L

D



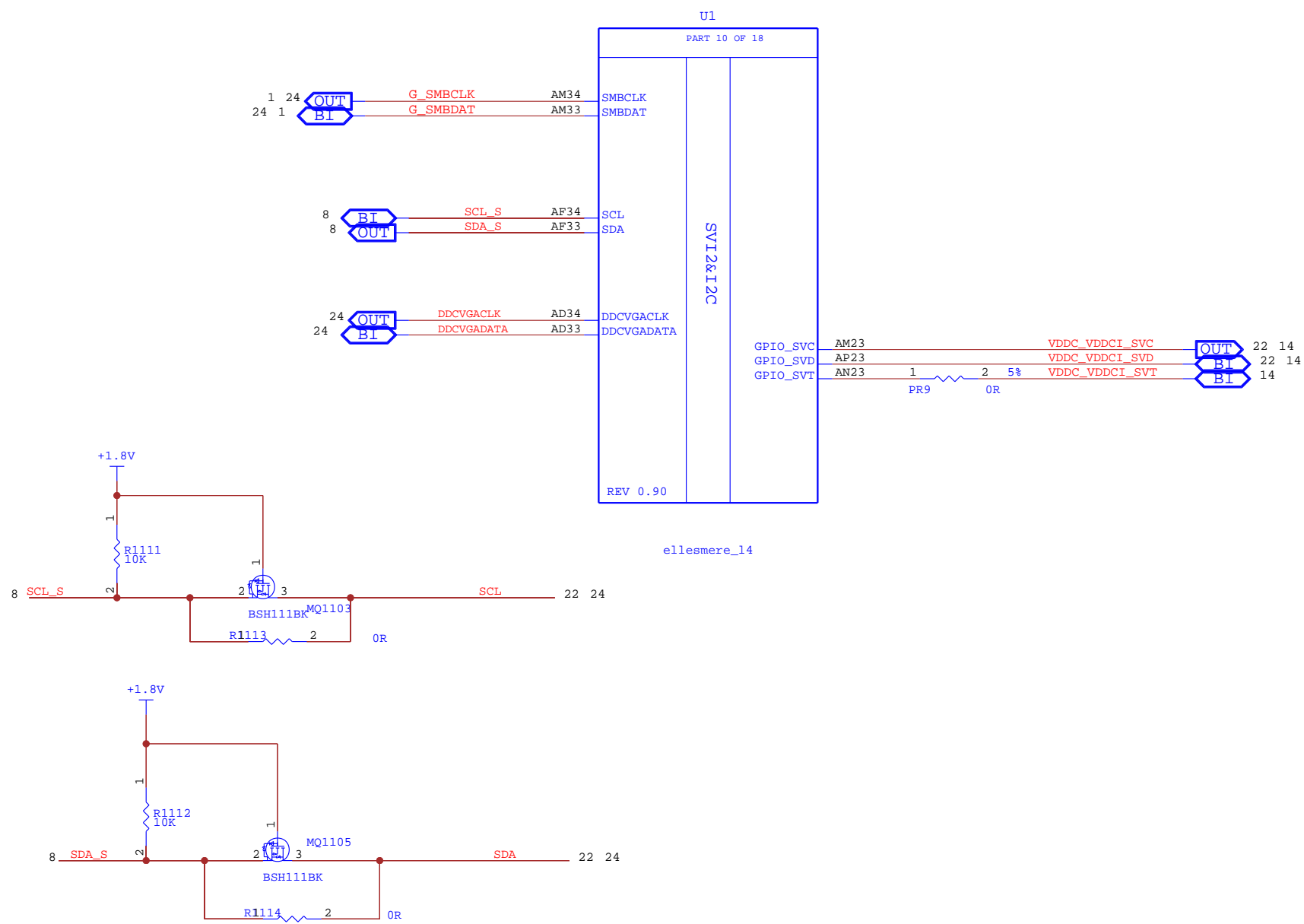
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(8) ELLESMERE DAC1 LOCK



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SHEET: ELLESMERE SV12&I2C		TITLE: Ellesmere XT/PRO GD5 8L	
DATE: Wed Apr 13 17:07:28 2016		REV: 1.0	
SHEET NUMBER: 8 OF 26			
DOCUMENT NUMBER: 105_D009XX_00			

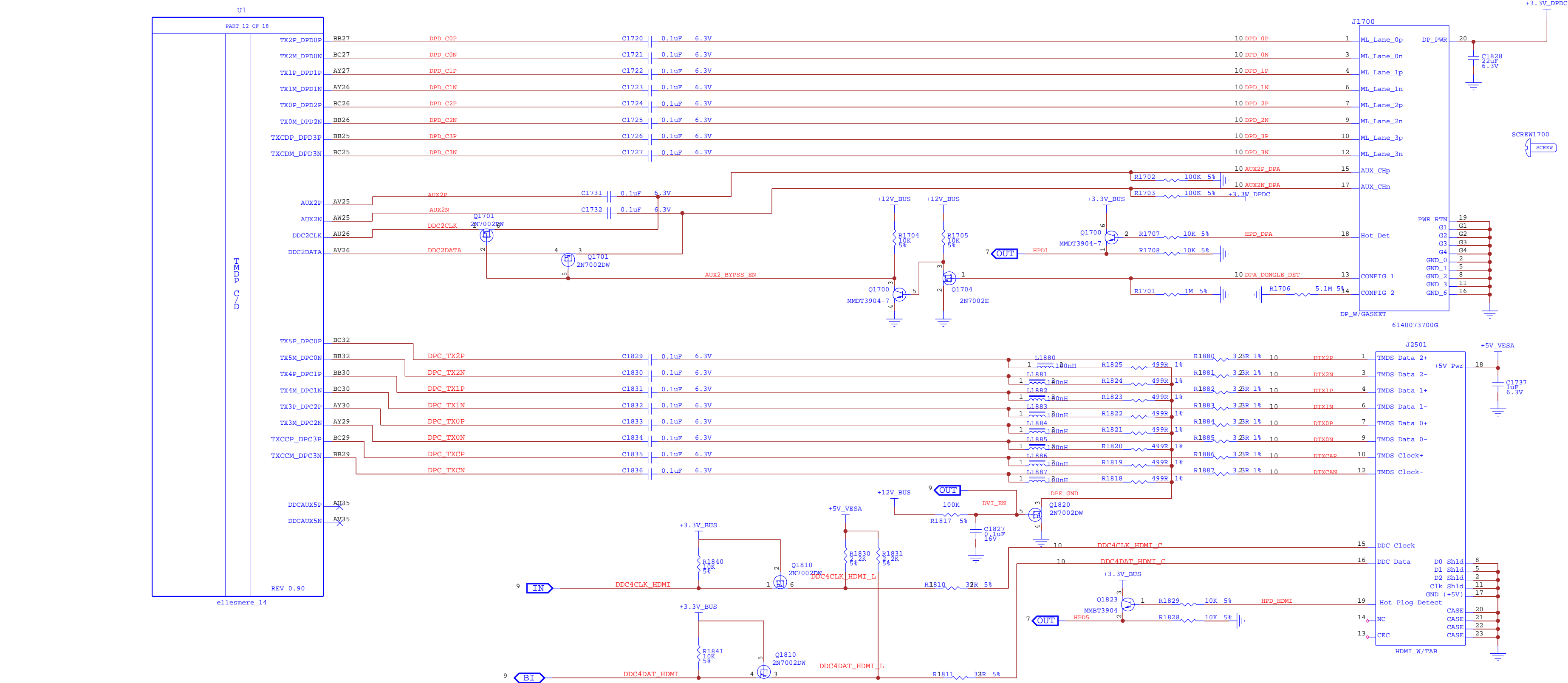
D



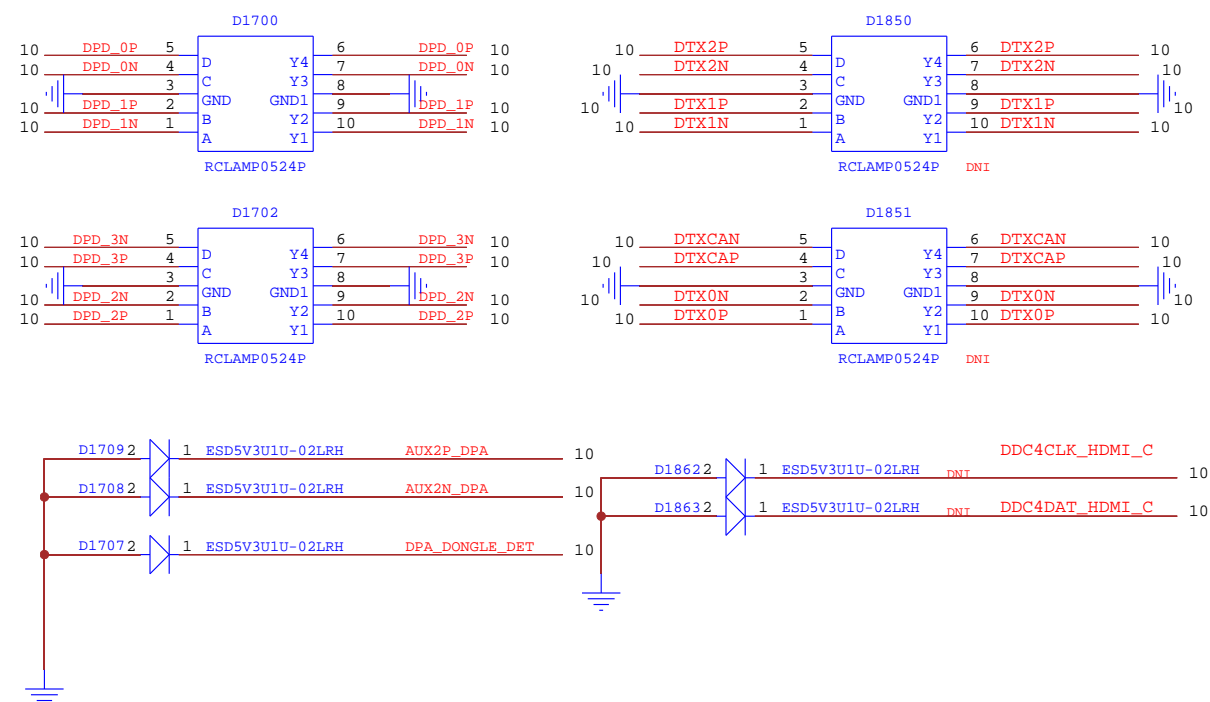
B

A

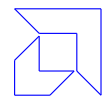
(10) ELLESMERE TMDP C/D



OPTIONAL ESD PROTECTION DIODES



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SHEET: ELLESMERE TMDPCD DP HDMI

DATE: Wed Apr 13 17:02:09 2016

REV: 1.0

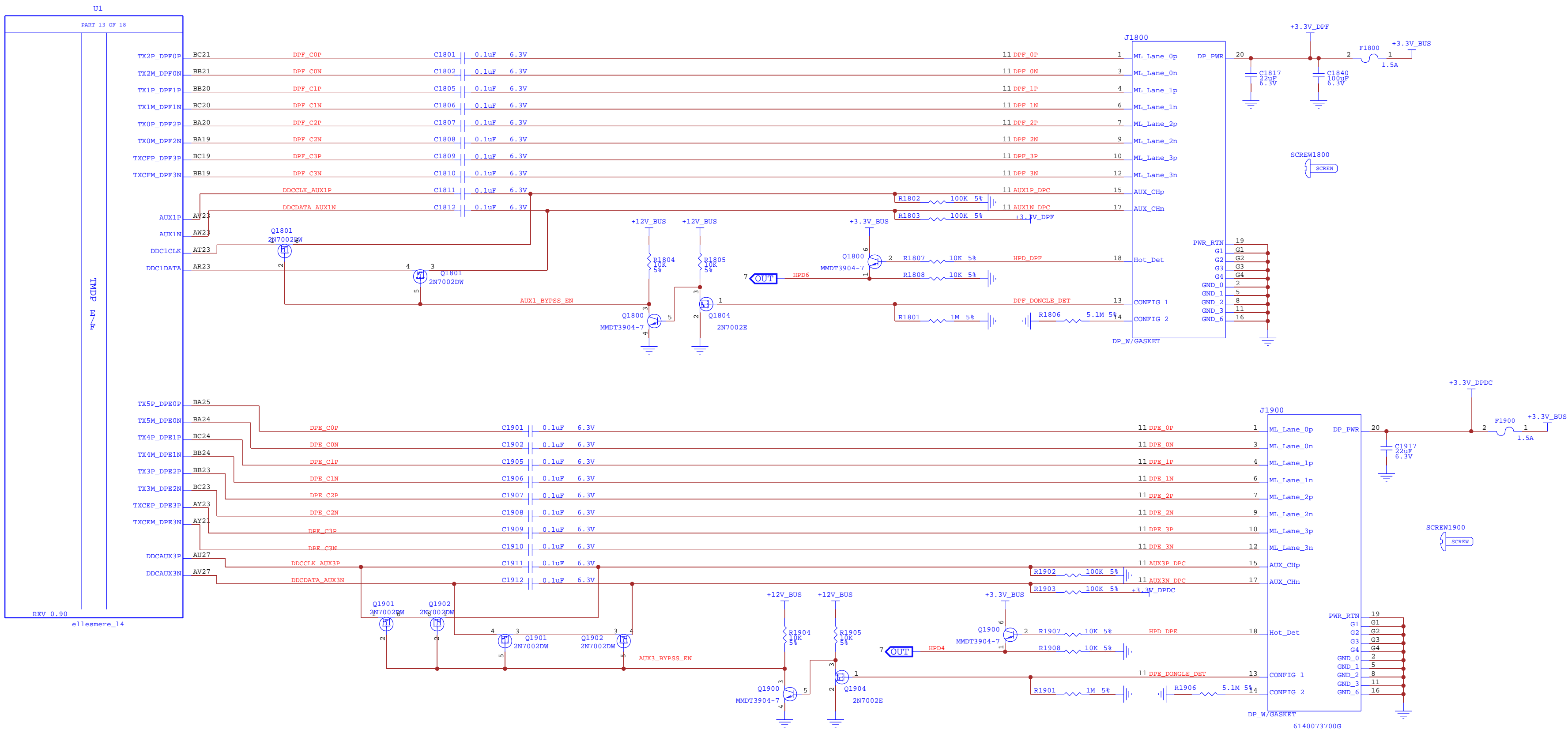
SHEET NUMBER: 10 OF 26

TITLE:

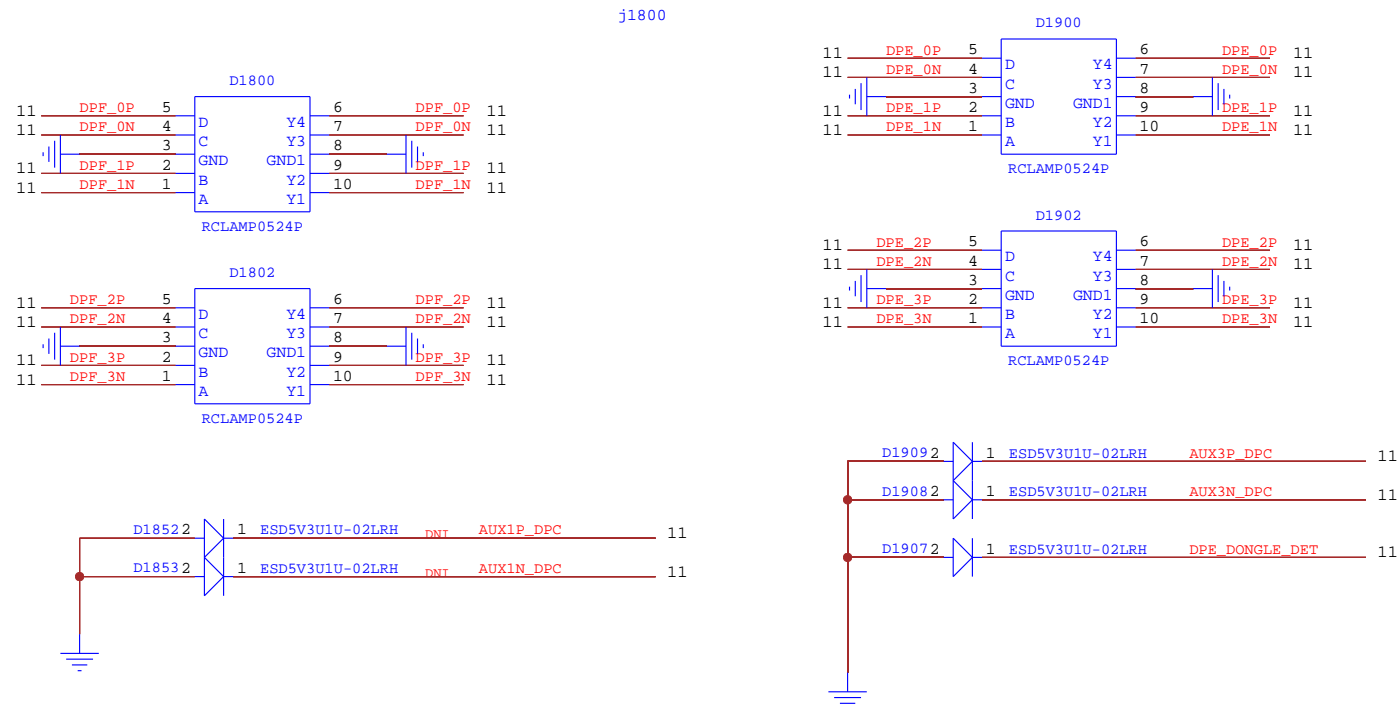
Ellesmere XT/PRO GD5 8L

DOCUMENT NUMBER: 105_D009XX_00

(11) ELLESMERE LVTMDP E/F



OPTIONAL ESD PROTECTION DIODES



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SHEET: ELLESMERE TMDP DP DP		TITLE: Ellesmere XT/PRO GD5 8L	
DATE: Wed Apr 13 17:02:09 2016		REV: 1.0	
SHEET NUMBER: 11 OF 26			
DOCUMENT NUMBER: 105_D009XX_00			

(12) ELLESMERE POWER

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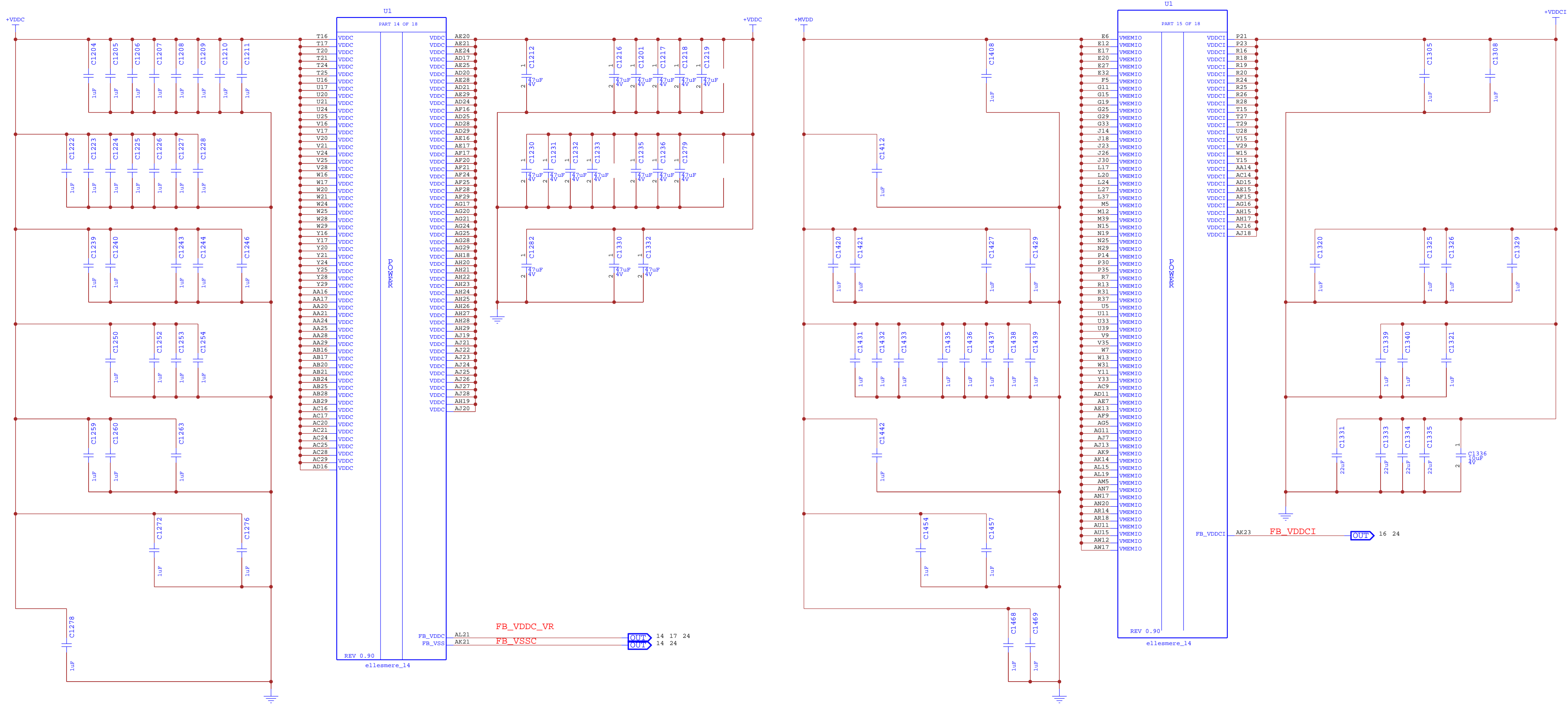
A

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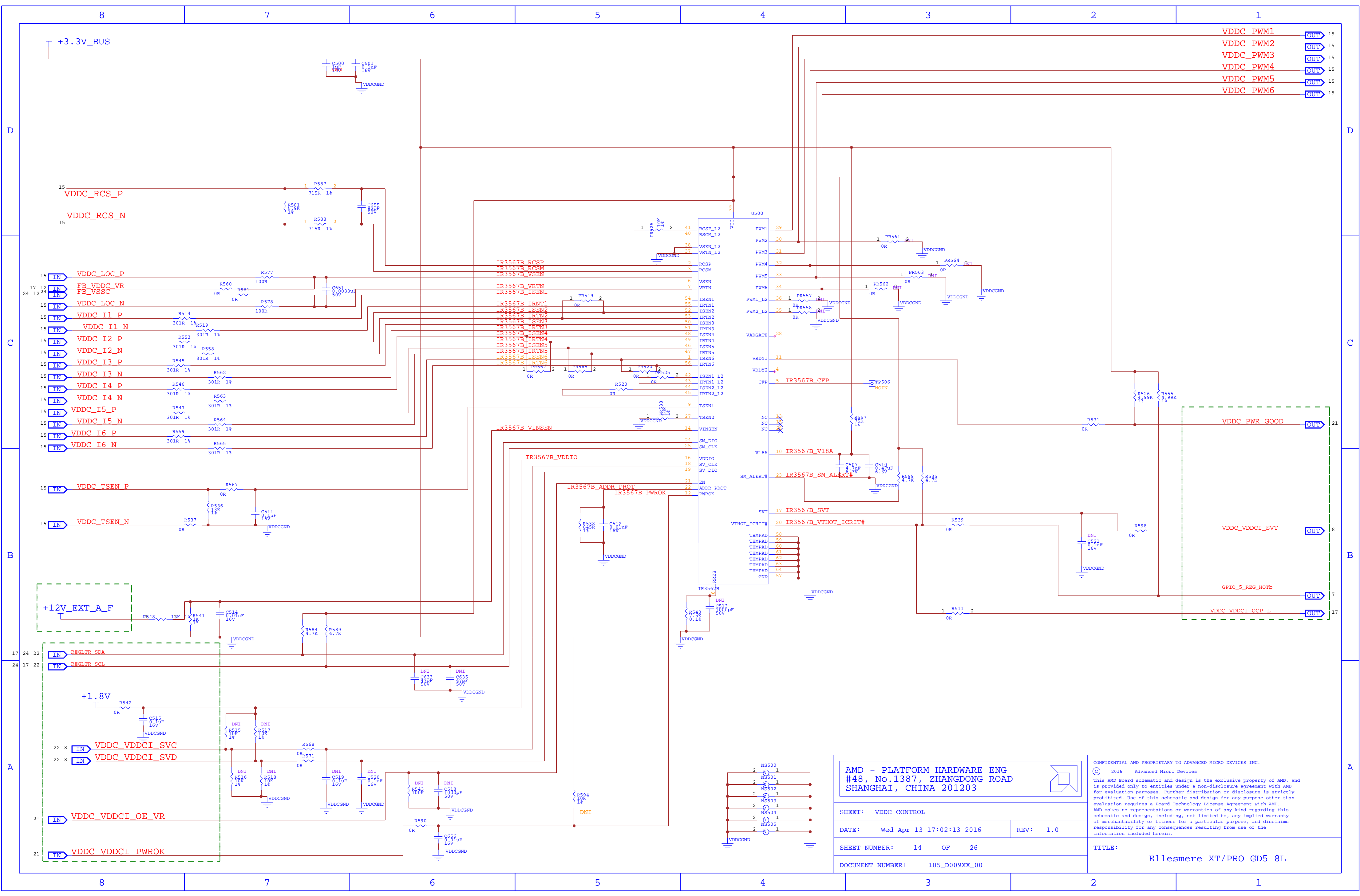


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SHEET: ELLESMERE POWER		TITLE: Ellesmere XT/PRO GD5 8L	
DATE: Wed Apr 13 17:02:18 2016		REV: 1.0	
SHEET NUMBER: 12 OF 26			
DOCUMENT NUMBER: 105_D009XX_00			

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SHEET: VDDC CONTROL		TITLE: Ellesmere XT/PRO GD5 8L	
DATE: Wed Apr 13 17:02:13 2016		REV: 1.0	
SHEET NUMBER: 14 OF 26			
DOCUMENT NUMBER: 105_D009XX_00			

VDDC

PLACE NEXT TO PHASE 3 HALF BRIDGE

PLACE BETWEEN PHASE 1 AND 2 INDUCTORS

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TITLE:

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SHEET: VDDC

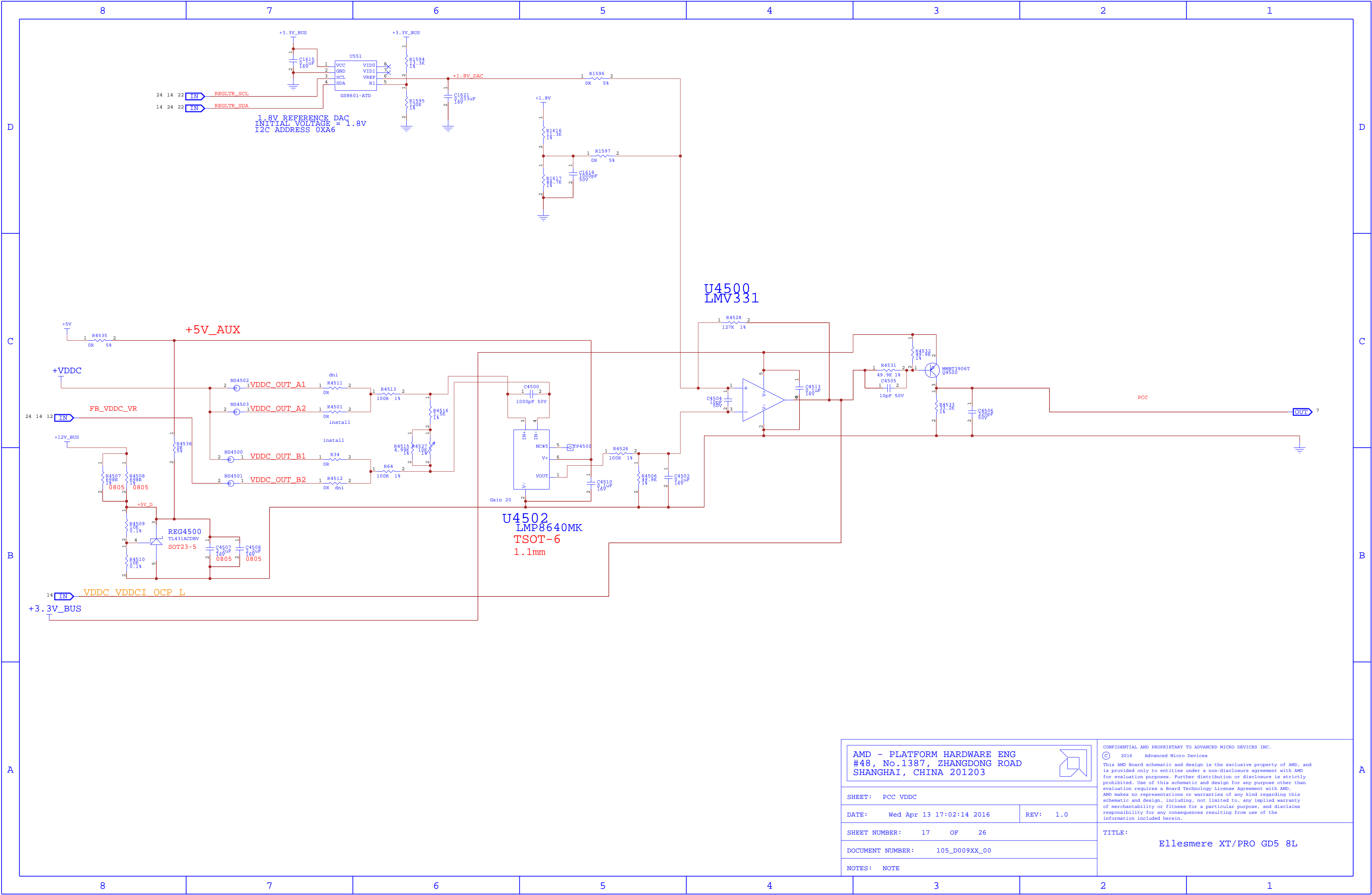
DATE: Wed Apr 13 17:02:14 2016

REV: 1.0

SHEET NUMBER: 15 OF 26

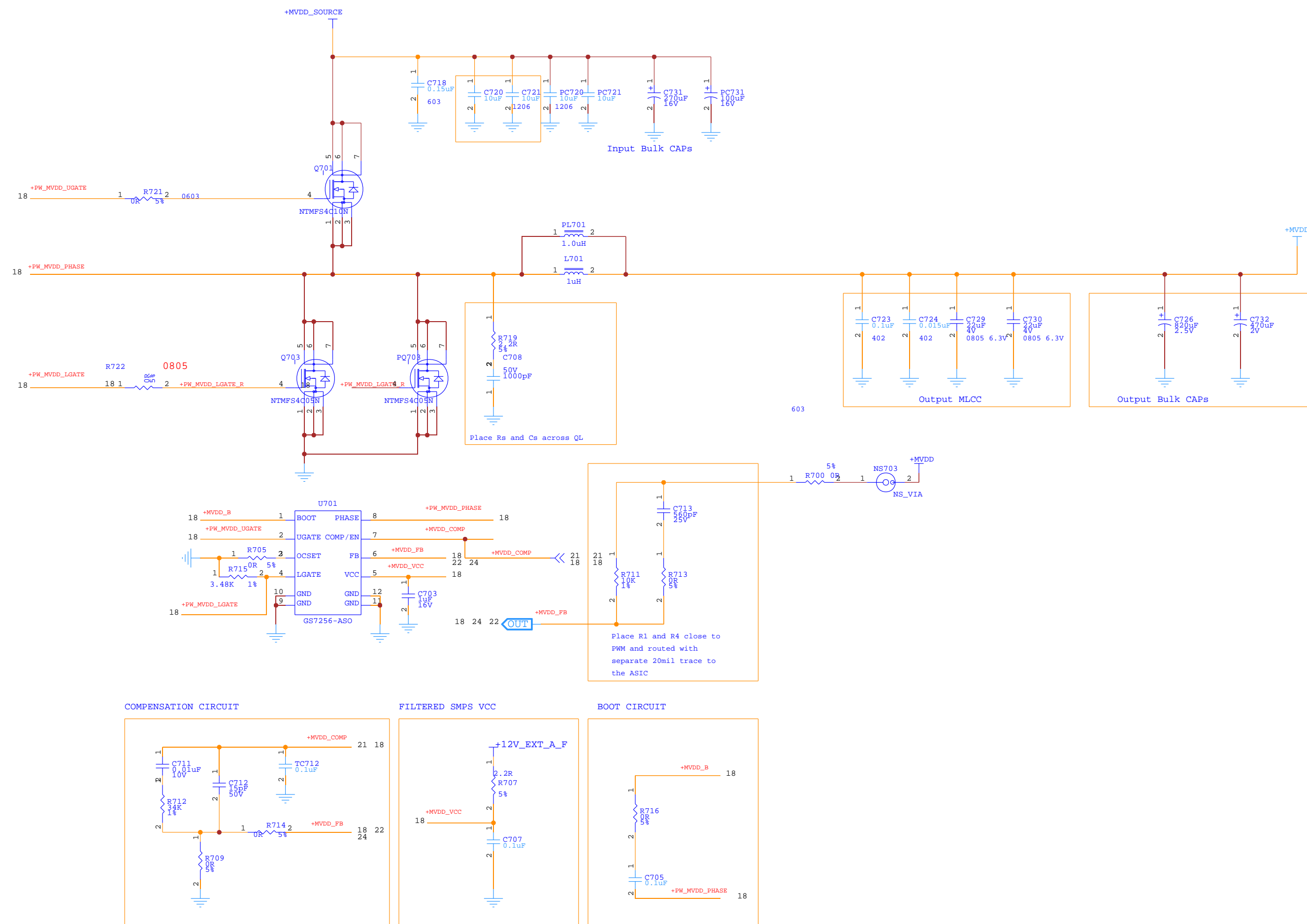
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NOTES: NOTE

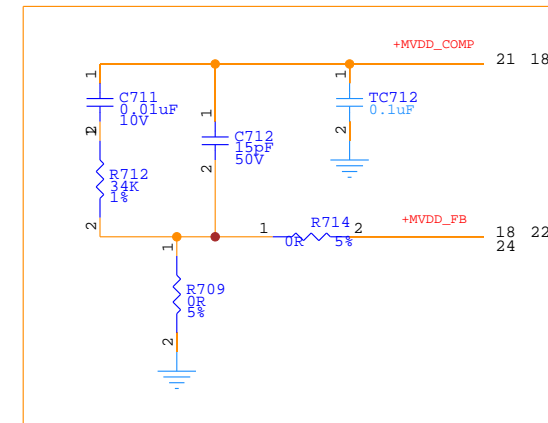


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SHEET: PCC VDDC		TITLE: Ellesmere XT/PRO GD5 8L	
DATE: Wed Apr 13 17:02:14 2016		REV: 1.0	
SHEET NUMBER: 17 OF 26			
DOCUMENT NUMBER: 105_D009XX_00			
NOTES: NOTE			

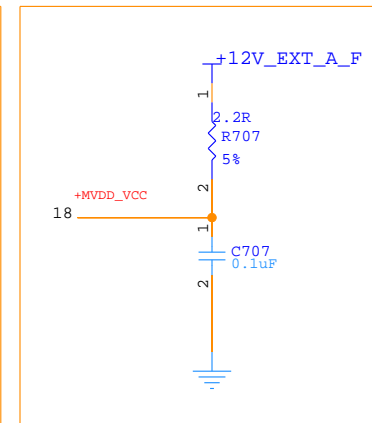
(16) MVDD



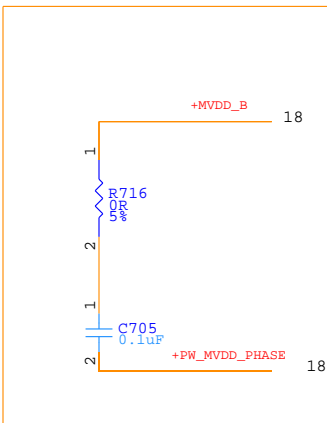
COMPENSATION CIRCUIT



FILTERED SMPS VCC



BOOT CIRCUIT



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SHEET: MVDD

DATE: Wed Apr 13 17:02:10 2016

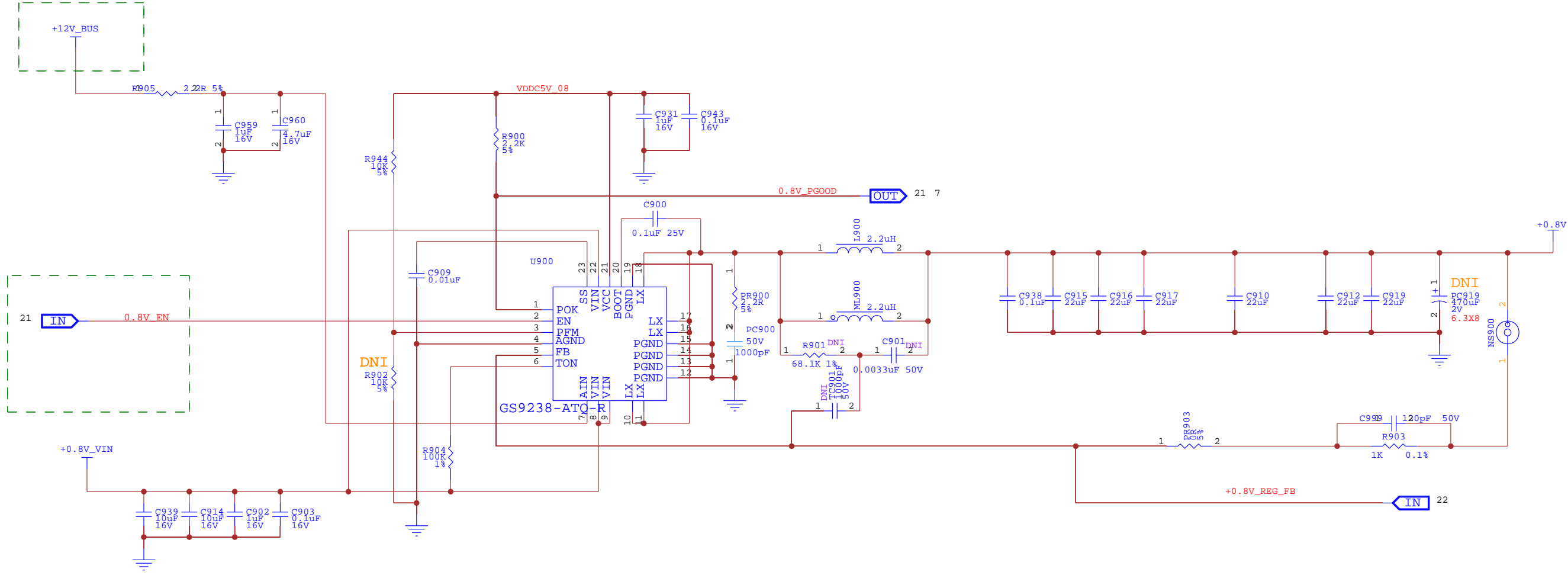
REV: 1.0

SHEET NUMBER: 18 OF 26

TITLE:

Ellesmere XT/PRO GD5 8L

(17) 0.95V



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SHEET: 0.8V REG

DATE: Wed Apr 13 17:02:11 2016

SHEET NUMBER: 19 OF 26

DOCUMENT NUMBER: 105 D009XX 00

NOTES: NOTE

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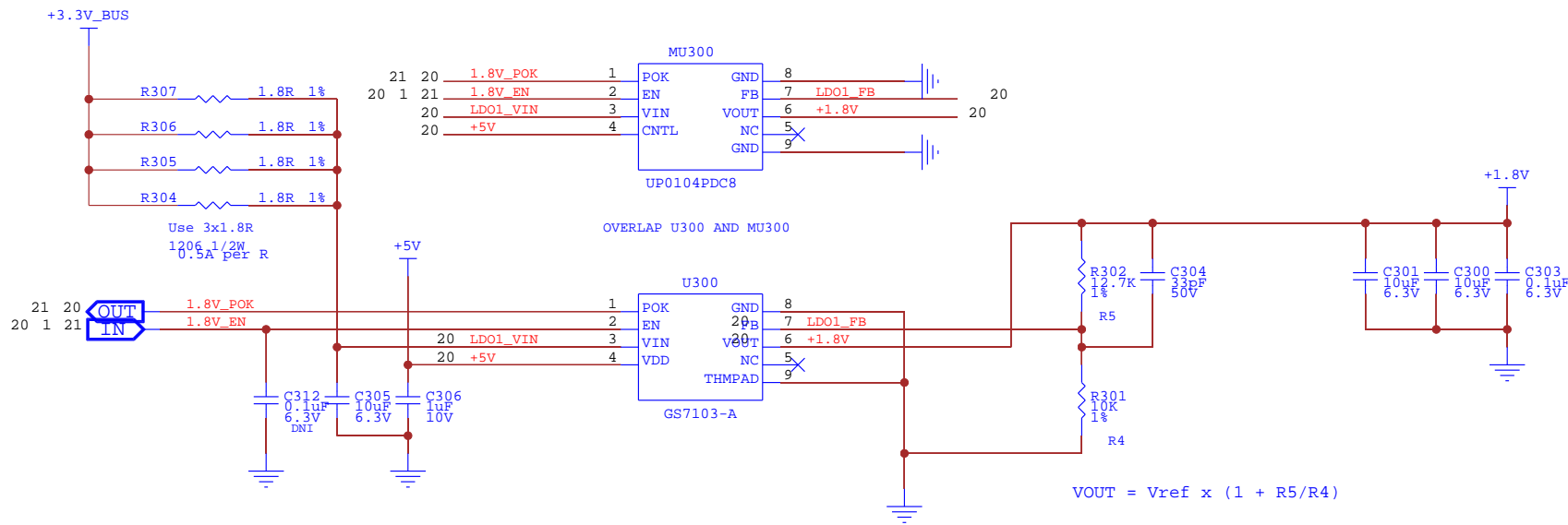
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TITLE:

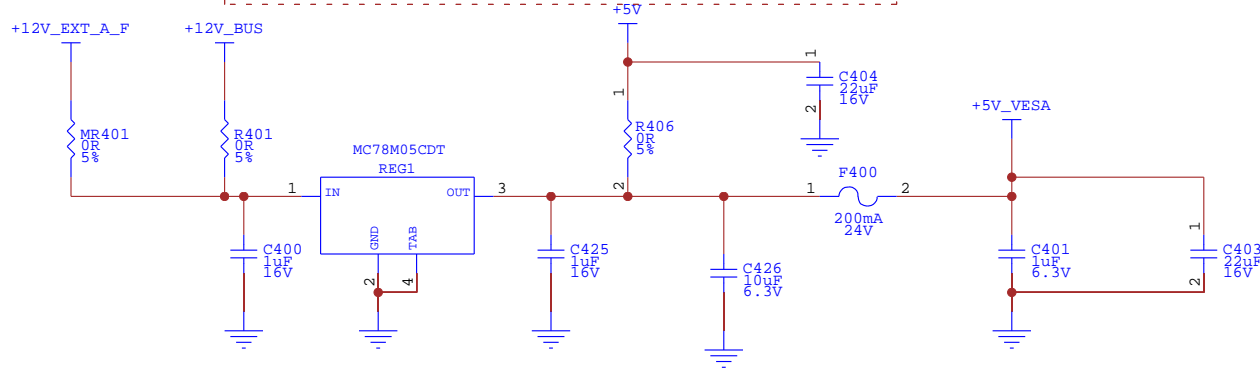
Ellesmere XT/PRO GD5 8L

(18) SMALL RAIL REGULATORS

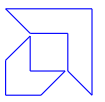
LDO #1: VIN = 3.0V TO 3.6V MAX VOUT = +1.8V +/- 2% IOUT = 1.3A RMS MAX
PCB: 50 TO 70mm SQ. COPPER AREA FOR COOLING



REGULATOR FOR +5V RAILS
IOUT MAX = 150mA



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SHEET: SMALL RAIL REGULATORS

DATE: Wed Apr 13 17:02:11 2016

REV: 1.0

SHEET NUMBER: 20 OF 26

DOCUMENT NUMBER: 105_D009XX_00

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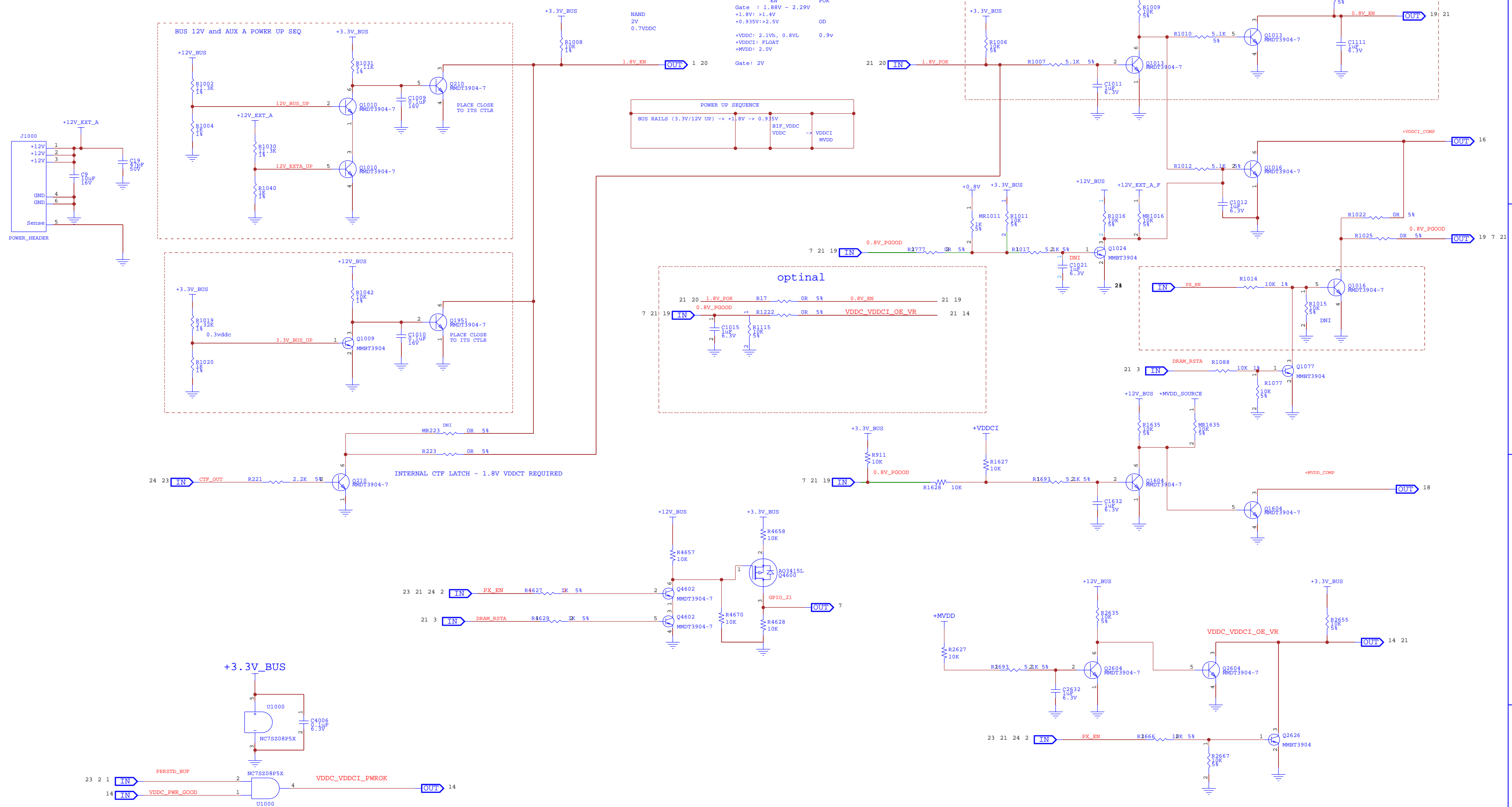
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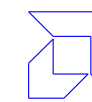
TITLE:

Ellesmere XT/PRO GD5 8L

(19) POWER MANAGEMENT



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SHEET: POWER MANAGEMENT1

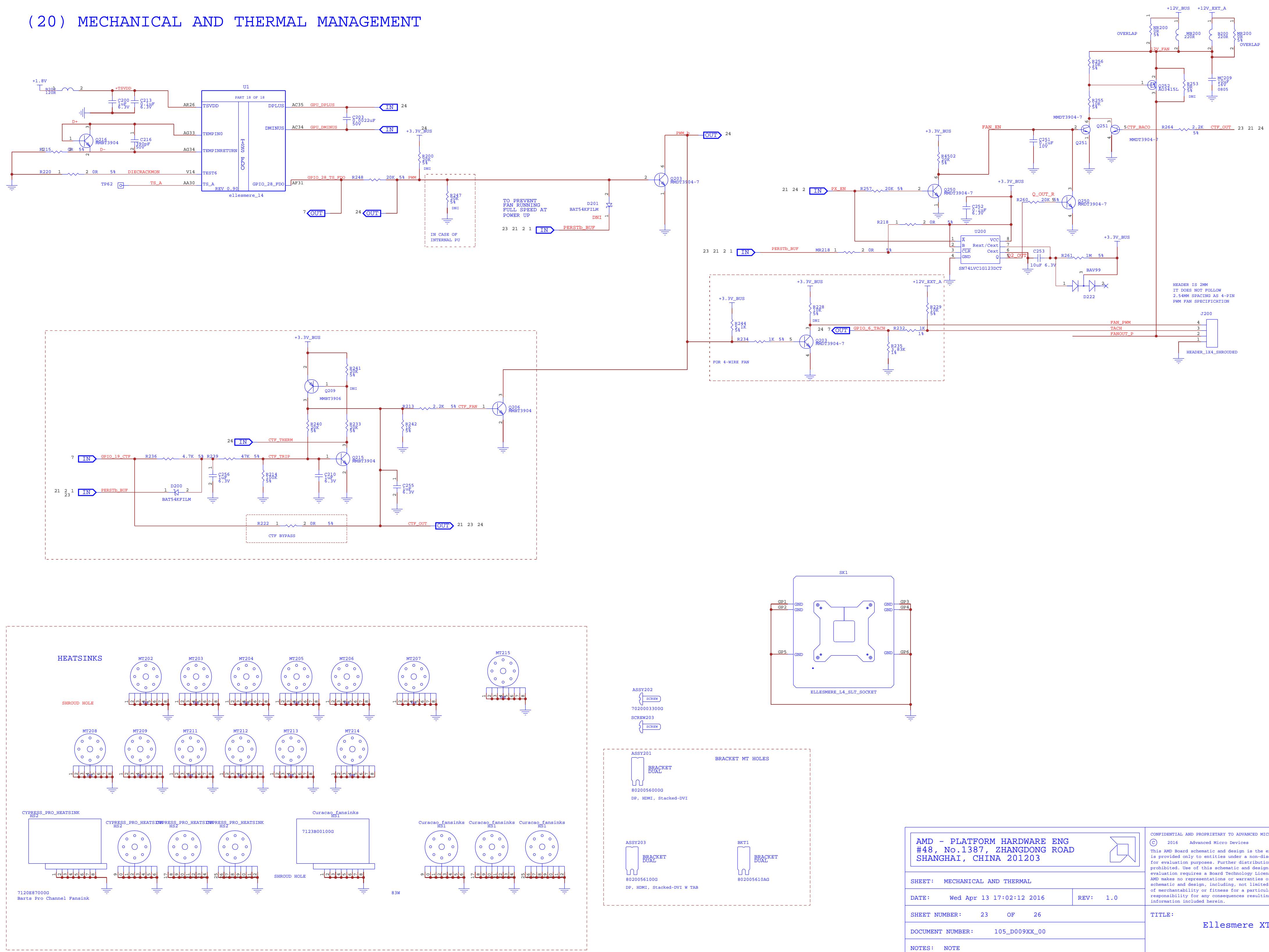
DATE: Wed Apr 13 17:05:37 2016

SHEET NUMBER: 21 OF 26

TITLE:

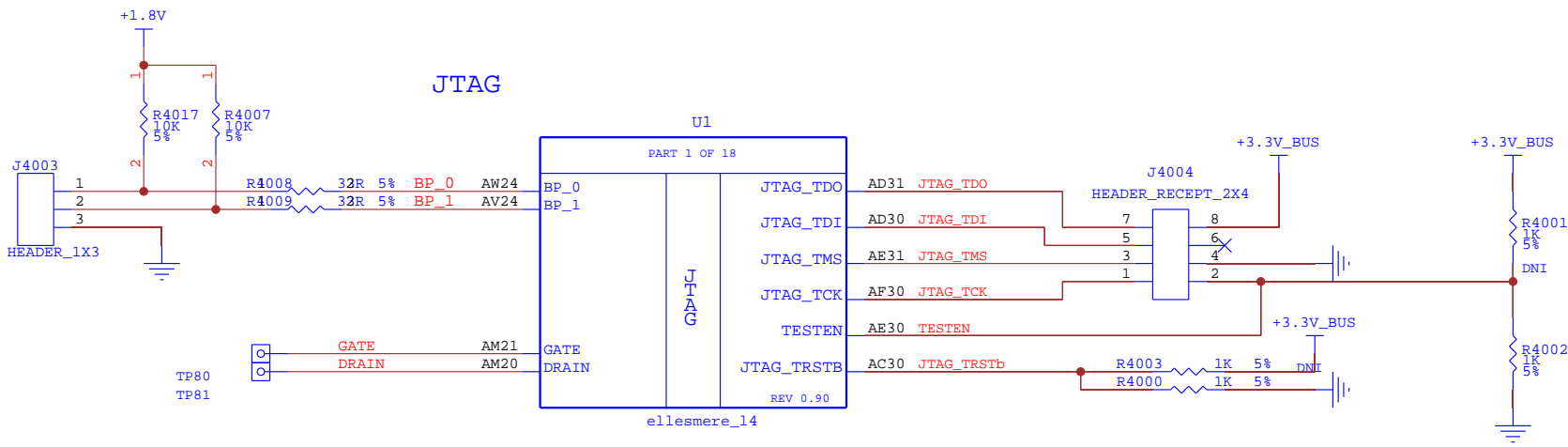
Ellesmere XT/PRO GD5 8L

(20) MECHANICAL AND THERMAL MANAGEMENT



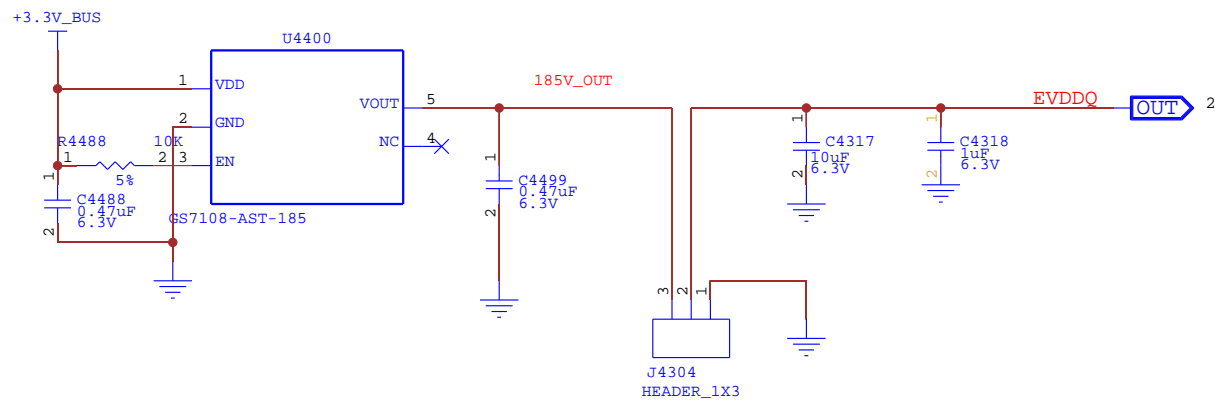
(21) DEBUG CIRCUITS

D

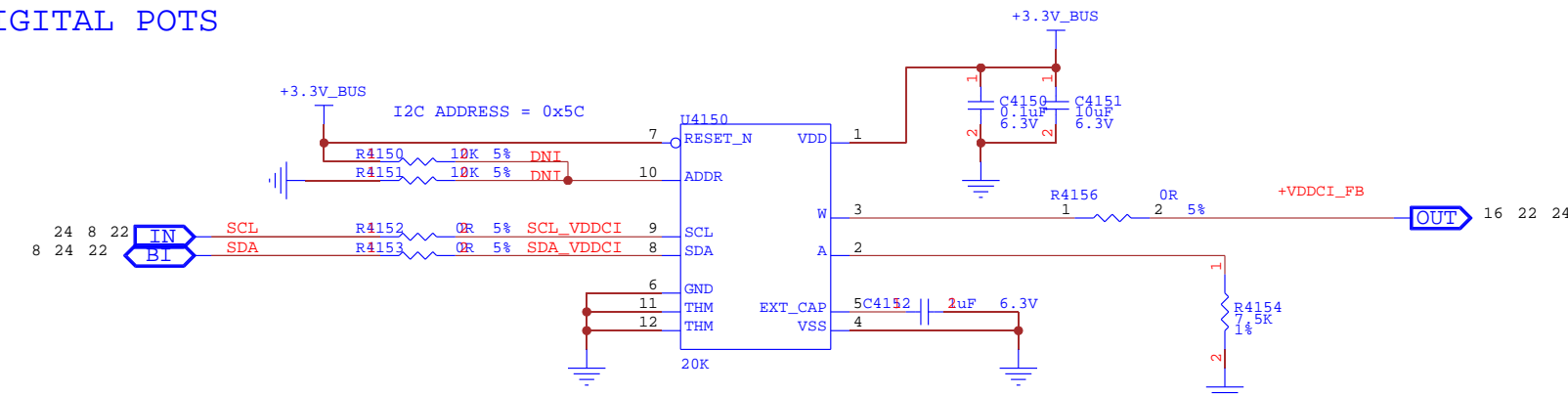


C

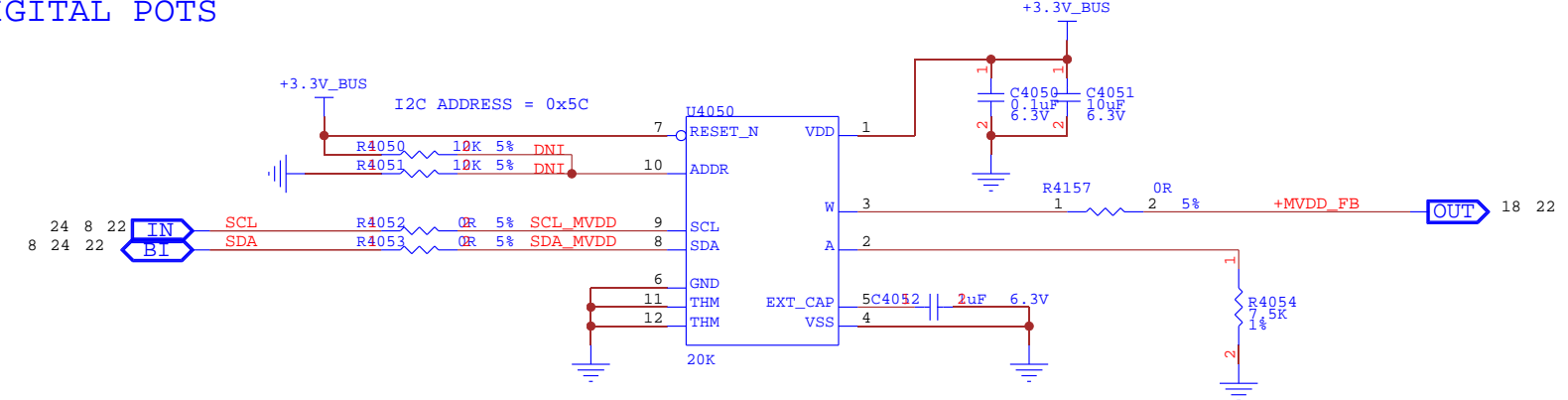
E-FUSE CAPABILITY
DEFAULT = GPIO-CONTROLLED
(MANUAL OPTION AS BACK-UP)



DIGITAL POTS

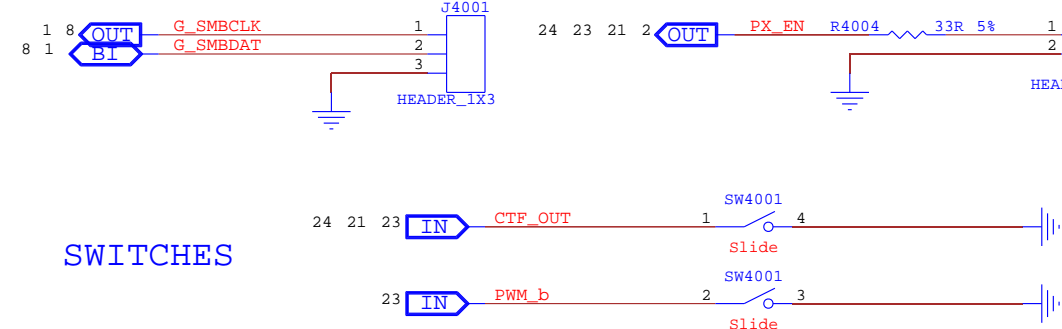


DIGITAL POTS

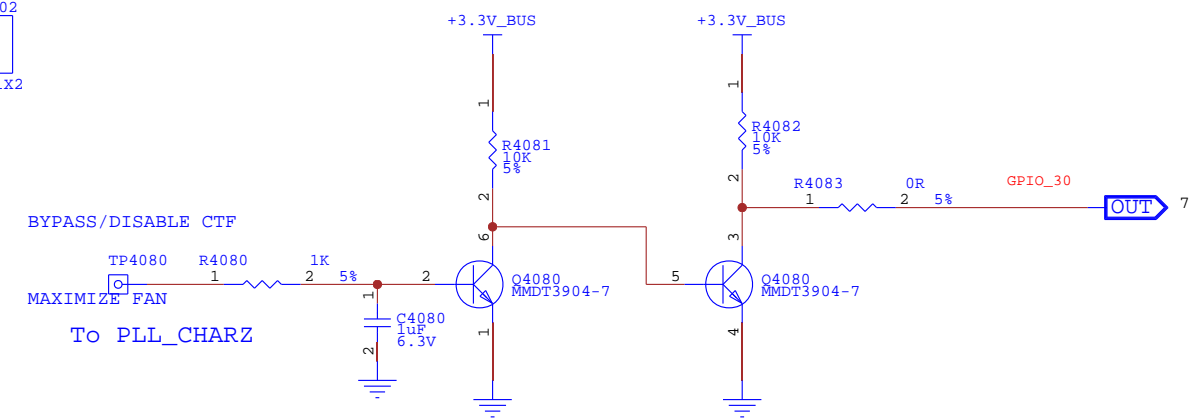


A

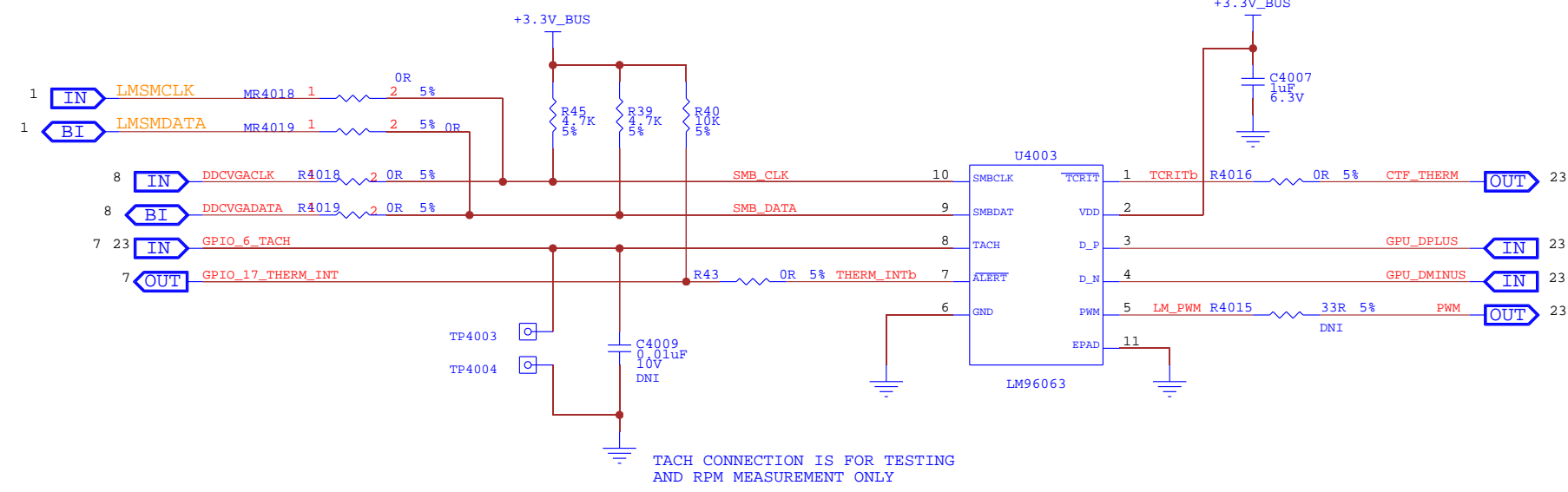
SWITCHES



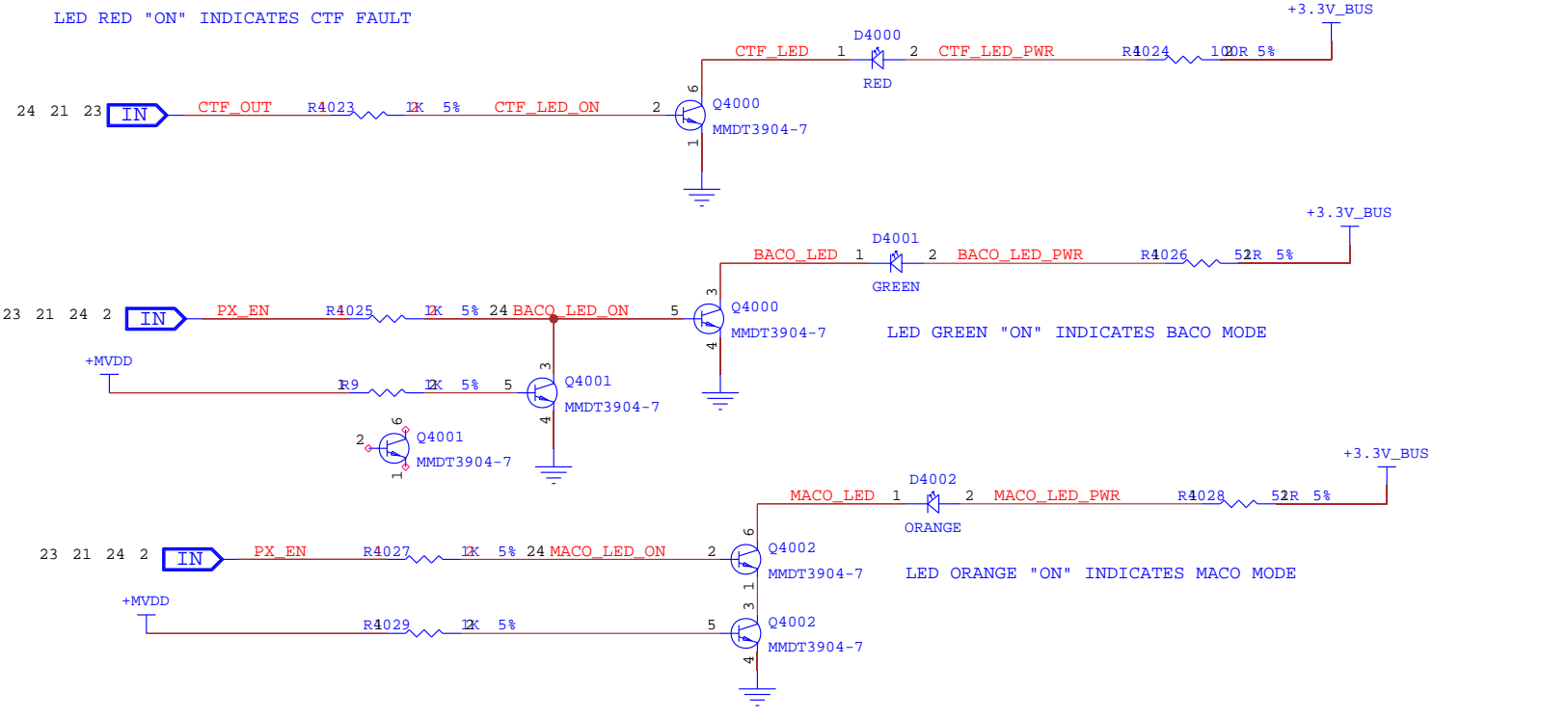
BYPASS/DISABLE CTF
MAXIMIZE FAN
TO PLL_CHARZ



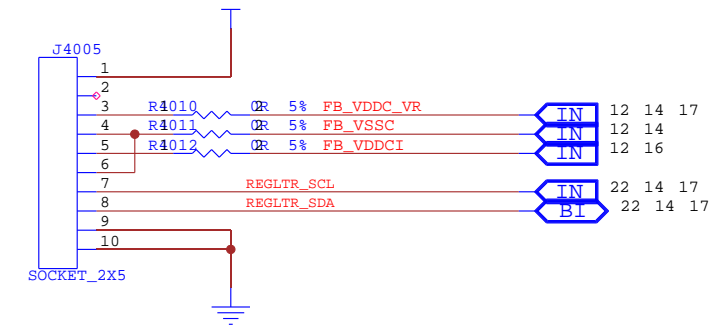
LM96163 FOR BACKUP THERMAL CONTROL



LED LIGHTS



VTMM - TEST CONNECTOR FOR VOLTAGE MEASUREMENTS



AMD - PLATFORM HARDWARE ENG
#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET: DEBUG CIRCUITS/E-FUSE

DATE: Wed Apr 13 17:02:12 2016

REV: 1.0

SHEET NUMBER: 24 OF 26

DOCUMENT NUMBER: 105_D009XX_00

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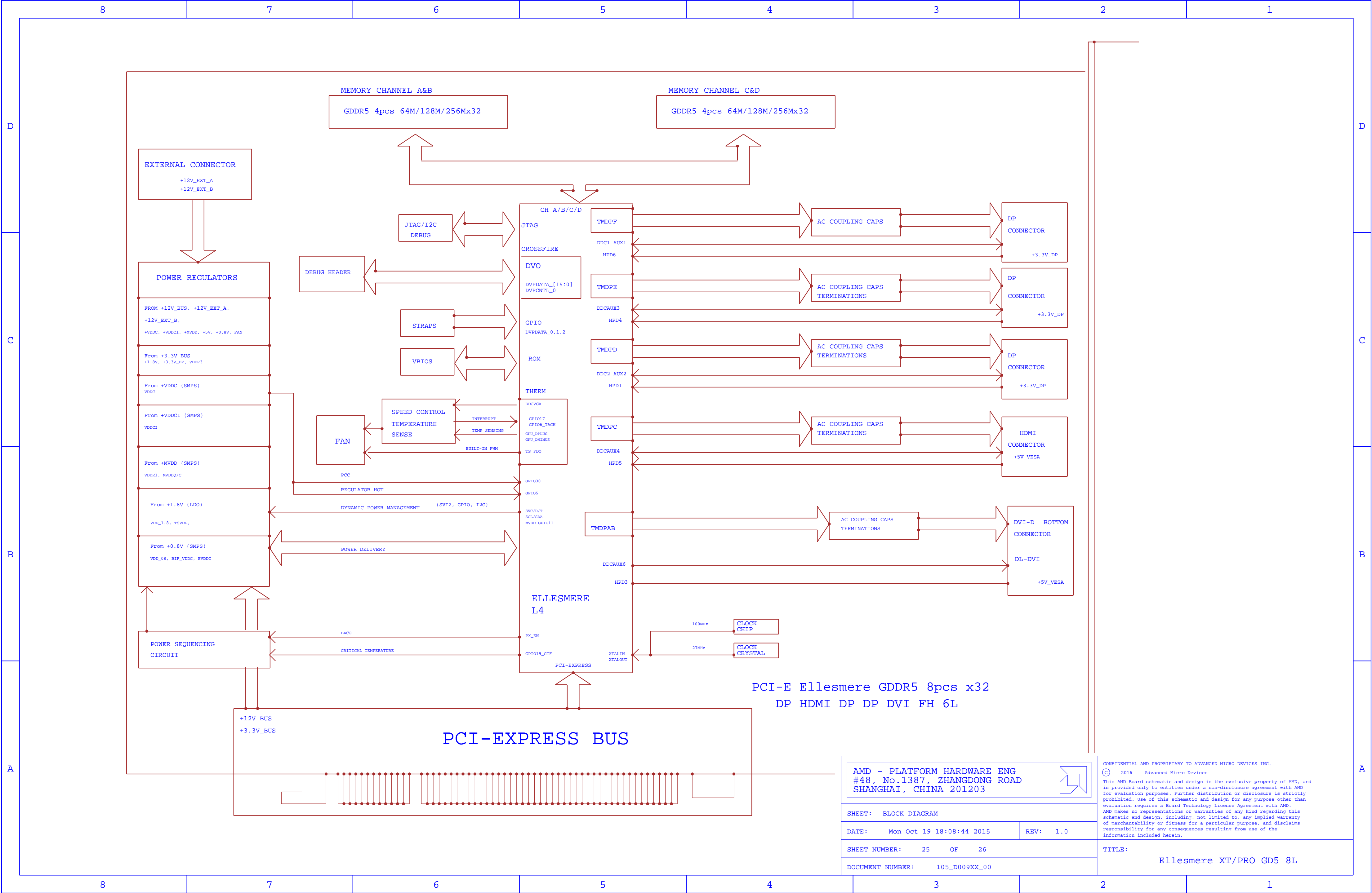
TITLE:
Ellesmere XT/PRO GD5 8L

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8			7			6			5			4			3			2			1																	
<div>AMD</div>			<div></div>			TITLE: Ellesmere XT/PRO GD5 8L						DOCUMENT NUMBER: 105_D009XX_00						DATE: Tue Apr 12 15:48:25 2016						SHEET NUMBER: 26 OF 26						REV: 1.0								
REVISION HISTORY									ENGINEER: XXX						NOTES: NOTE						CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRO DEVICES INC. C • 2016 Advanced Micro Devices This AMD Board schematic and design is the exclusive property of AMD, and is provided only to entities under a non-disclosure agreement with AMD for evaluation purposes. Further distribution or disclosure is strictly prohibited. Use of this schematic and design for any purpose other than evaluation requires a Board Technology License Agreement with AMD. AMD makes no representations or warranties of any kind regarding this schematic and design, including, not limited to, any implied warranty of merchantability or fitness for a particular purpose, and disclaims responsibility for any consequences resulting from use of the information included herein.									AMD - PLATFORM HARDWARE ENG #48, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203								
SCH Rev			PCB Rev			Date			REVISION DESCRIPTON																													
0			00A			08/28/2015																																
1			00B			10/19/2015			1. Add GPIO1 for dynamic VDDCI 2. update VDDC driver sequence																													
2			00C			03/23/2016			Same as D000 REVC schematic HDMI: - add series resistor R1880 ~R1887 - add pull down inductor L1880 ~L1887 remove C405, C403, VR400,C410,C414,R405																													
3			00			04/12/2016			1. Add EMC1412																													
8			7			6			5			4			3			2			1																	